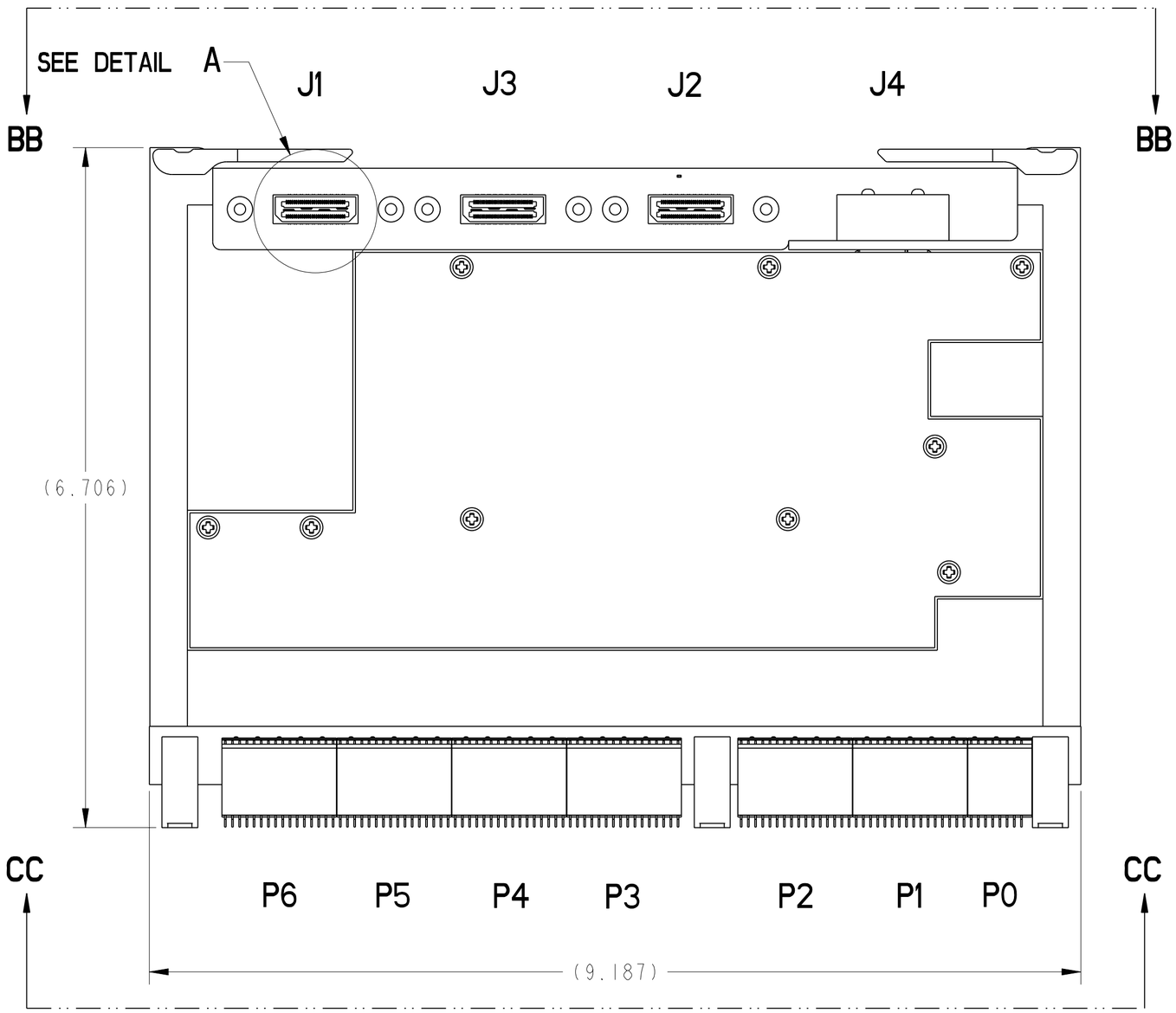
DOCUMENT NO.	SHEET	REV.
CF-020010-57X	SHEET 1 OF 8	A

REVISIONS		
LTR	DESCRIPTION	DATE
A	INITIAL RELEASE	---



ATTENTION
OBSERVE PRECAUTIONS
FOR HANDLING
ELECTROSTATIC
SENSITIVE
DEVICES

NOTES: SEE SHEET 3

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DOCUMENT NO.	SHEET	REV.
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N/A

NEXT ASSEMBLY

PRO/ENGINEER INFORMATION
Pro/e Model Used:
CF-020010-57X.ASSEM
Drawing Name:
CF-020010-57X

UNLESS OTHERWISE SPECIFIED
LINEAR DIMENSIONS ARE IN INCHES
TOLERANCES:
.XXXX = ±.0005 ANGLES= ±2°
.XXX = ±.010
.XX = ±.03
.X = ±.1

DIM.& TOL. PER ASME Y14.5M;
DRM PER MIL-DTL-31000;
OTHER Amphenol Stds. PER 9-3800
LEGENDS:
=FLAG NOTE CALL OUT
REFERENCE ONLY

SPECIFICATIONS
MATERIAL SPEC.
N/A
PROCESS SPEC.
SEE NOTES 1 & 3

POS	QTY	PART NUMBER	DESCRIPTION	NOTE

PARTS LIST

APPROVALS		DATE
PREPARED BY	M.GLYNN	11-Apr-16
ENGINEER IN CHARGE	J.PAUL	
DESIGN MANAGER	J.PAUL	
DESIGN ACTIVITY GROUP	C1	

THIRD ANGLE PROJECTION

AMPHENOL CORPORATION
40-60 DELAWARE AVENUE
SIDNEY, N.Y. 13838

VPX 48.2 MEDIA CONVERTER

SIZE	CAGE CODE	DOCUMENT NO.	REV.
C	77820	CF-020010-57X	A

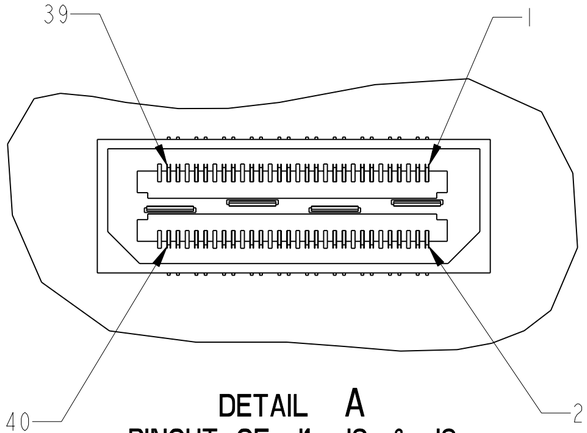
SCALE: 1.0 | REF. NONE | SHEET 1 OF 8

FORMAT: C-U.E-1

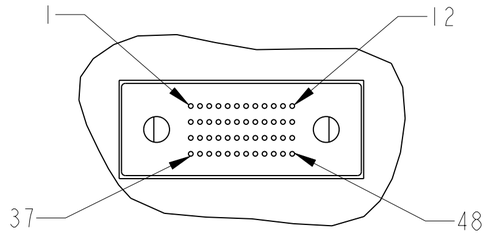
CF-020010-57X | SHEET 1 OF 8 | REV. A

VERSION 4 | REVISION A | Eng PDM Information For Reference Only

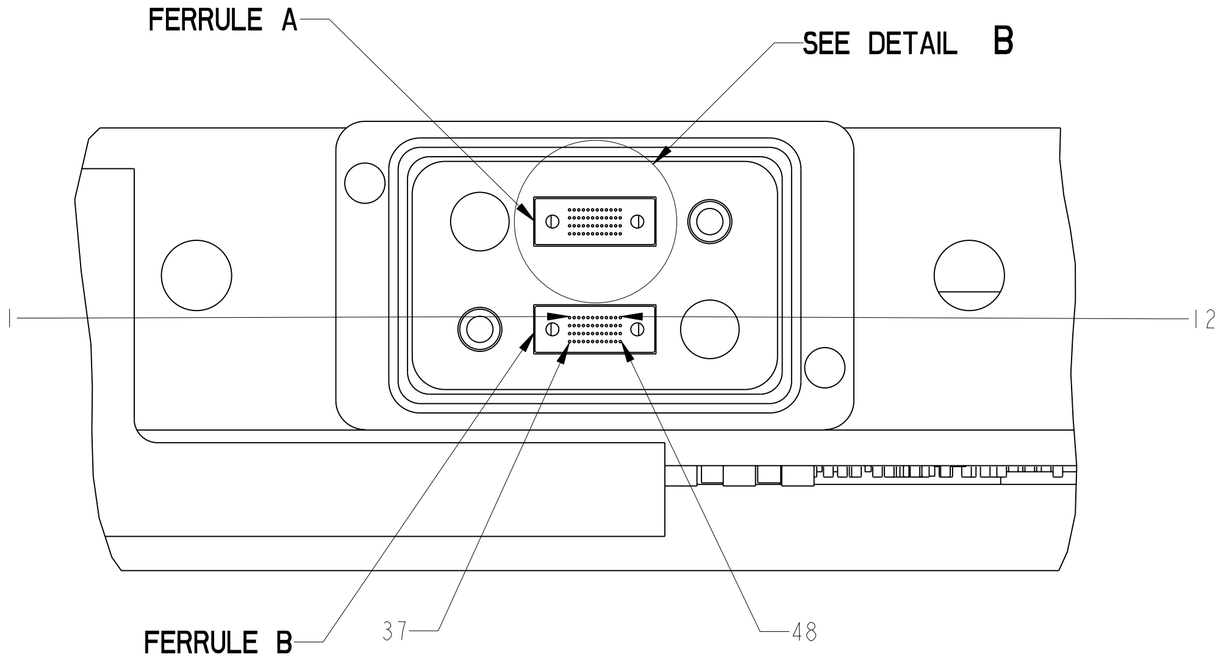
REV. A | SHEET 1 OF 8 | DOCUMENT NO. CF-020010-57X



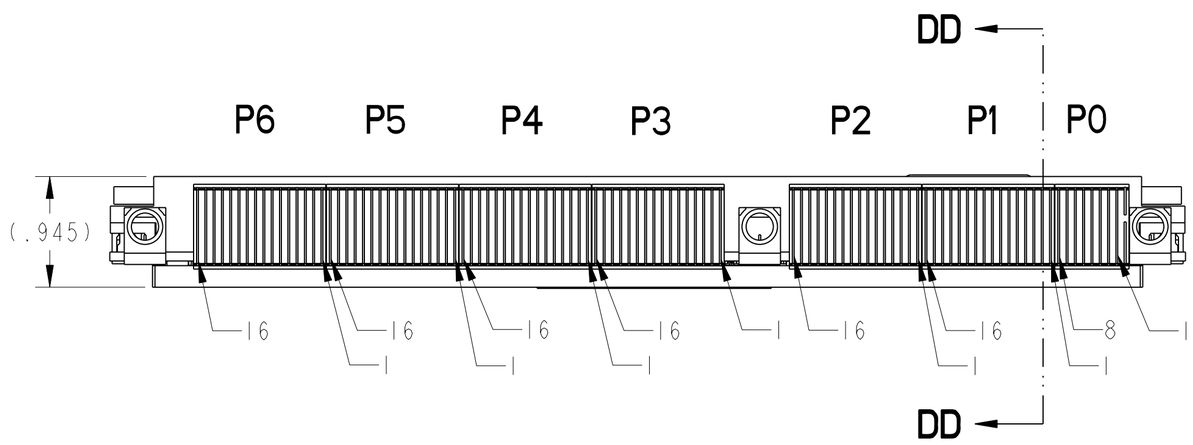
DETAIL A
PINOUT OF J1, J2, & J3
SCALE 4.000



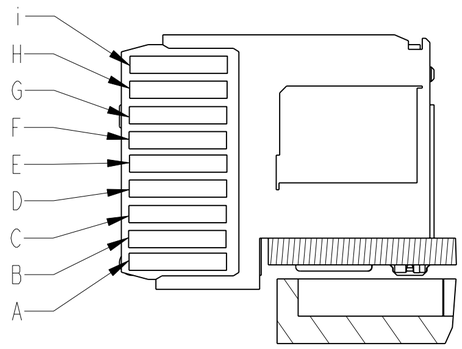
DETAIL B
SCALE 8.000



PARTIAL ROTATED VIEW AT BB-BB
PINOUT OF J4
SCALE 4.000



VIEW AT CC-CC
WAFER COLUMN DESIGNATION



SECTION DD-DD
WAFER PAD DESIGNATIONS
SCALE 3.000

NOTES: SEE SHEET 3

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DOCUMENT NO.	SHEET 2 OF 8	REV.
CF-020010-57X		A

SIZE	CAGE CODE	DOCUMENT NO.	REV.
C	77820	CF-020010-57X	A
SCALE: 1.0	REF: NONE	SHEET 2 OF 8	

CF-020010-57X
SHEET 2 OF 8
REV. A

VERSION 4
REVISION A
Eng PDM Information For Reference Only

CONNECTION LIST

(4)

FROM				CONVERSION	TO			
VPX CONNECTOR	DESCRIPTION	INPUT / OUTPUT	PIN		DESCRIPTION	INPUT / OUTPUT	PIN	CONNECTOR
P0	12V	INPUT	A1-A2, B1-B2, C1-C2, D1-D2, F1-F2, G1-G2, H1-H2, i1-i2	N/A				
	5V	INPUT	A3, B3, C3, D3, F3, G3, H3, i3					
	RESET	INPUT	C4					
	GND	OUTPUT	A4-A6, A8, B8, C7, D4-D7, E8, F4-F6, F8, G7, H7, i4-i6, i8					

5. MEDIA CONVERTER MEETS ALL APPLICABLE REQUIREMENTS OF VITA 48.2

4. SEE CONNECTION LIST FOR PINOUT INFORMATION.

3. INK STAMP AMPHENOL , PART NUMBER, 6 DIGIT SERIAL NUMBER
   ON INDICATED SURFACE. CHARACTERS TO BE .062±.020 HIGH.
   INK STAMP PER 9-3856-5. SERIAL NUMBER PER 9-9172.
   EXAMPLE: (CONVERTER NUMBER 3 BUILT ON MARCH 21, 2016)
   AMPHENOL CF-020010-57X 161203

2. PARTS TO BE PACKAGED IN METALLIZED SHIELDING BAGS AND LABELED PER ANSI/ESD S541-2003.

1. ASSEMBLY CONTAINS ELECTROSTATIC DISCHARGE (ESD) SENSITIVE COMPONENTS. ASSEMBLY SHALL BE HANDLED, PACKAGED, AND SHIPPED TO MEET REQUIREMENTS OF ANSI/ESD-S-20.20 AND IPC-A-610.

NOTES:

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SCALE: 1.0	REF: NONE	SHEET 3 OF 8	

CF-020010-57X | SHEET 3 OF 8 | REV. A

Eng PDM Information For Reference Only | REVISION A | VERSION 4

FORMAT: C-U.E-1

CONNECTION LIST

(4)

FROM				CONVERSION	TO			
VPX CONNECTOR	DESCRIPTION	INPUT / OUTPUT	PIN		DESCRIPTION	INPUT / OUTPUT	PIN	CONNECTOR
P1	GND	OUTPUT	A2, A4, A6, A8, A10, A12, A14, A16, B2, B4, B6, B8, B10, B12, B14, B16, C1, C3, C5, C7, C9, C11, C13, C15, D1, D3, D5, D7, D9, D11, D13, D15, E2, E4, E6, E8, E10, E12, E14, E16, F2, F4, F6, F8, F10, F12, F14, F16, G1, G3, G5, G7, G9, G11, G13, G15, H1, H3, H5, H7, H9, H11, H13, H15, i2, i4, i6, i8, i10, i12, i14, i16					
	NC	N/A	A5, A7, B5, B7, C6, C8, E5, E7, F5, F7, H6, H8, i1, i3, i5, i7, i9, i11, i13					
	PORT1_IGBaseKX_RX + / -	INPUT	A1 / B1	IG PHY	PORT1_IGBaseT_DA + / -	BI	1 / 3	J2
	PORT1_IGBaseKX_TX + / -	OUTPUT	E1 / F1		PORT1_IGBaseT_DB + / -	BI	2 / 4	
					PORT1_IGBaseT_DC + / -	BI	5 / 7	
					PORT1_IGBaseT_DD + / -	BI	6 / 8	
	PORT2_IGBaseKX_RX + / -	INPUT	C2 / D2	IG PHY	PORT2_IGBaseT_DA + / -	BI	13 / 15	
	PORT2_IGBaseKX_TX + / -	OUTPUT	G2 / H2		PORT2_IGBaseT_DB + / -	BI	14 / 16	
					PORT2_IGBaseT_DC + / -	BI	17 / 19	
					PORT2_IGBaseT_DD + / -	BI	18 / 20	
	PORT3_IGBaseKX_RX + / -	INPUT	A3 / B3	IG PHY	PORT3_IGBaseT_DA + / -	BI	21 / 23	
	PORT3_IGBaseKX_TX + / -	OUTPUT	E3 / F3		PORT3_IGBaseT_DB + / -	BI	22 / 24	
					PORT3_IGBaseT_DC + / -	BI	25 / 27	
					PORT3_IGBaseT_DD + / -	BI	26 / 28	
	PORT4_IGBaseKX_RX + / -	INPUT	C4 / D4	IG PHY	PORT4_IGBaseT_DA + / -	BI	33 / 35	
	PORT4_IGBaseKX_TX + / -	OUTPUT	E4 / F4		PORT4_IGBaseT_DB + / -	BI	34 / 36	
					PORT4_IGBaseT_DC + / -	BI	37 / 39	
					PORT4_IGBaseT_DD + / -	BI	38 / 40	
	PORT5_IGBaseT_DA + / -	BI	A9 / B9	N/A	PORT5_IGBaseT_DA + / -	BI	1 / 3	J1
	PORT5_IGBaseT_DB + / -	BI	E9 / F9		PORT5_IGBaseT_DB + / -	BI	2 / 4	
	PORT5_IGBaseT_DC + / -	BI	C10 / D10		PORT5_IGBaseT_DC + / -	BI	5 / 7	
	PORT5_IGBaseT_DD + / -	BI	G10 / H10		PORT5_IGBaseT_DD + / -	BI	6 / 8	
	PORT6_IGBaseT_DA + / -	BI	A11 / B11		PORT6_IGBaseT_DA + / -	BI	13 / 15	
	PORT6_IGBaseT_DB + / -	BI	E11 / F11		PORT6_IGBaseT_DB + / -	BI	14 / 16	
	PORT6_IGBaseT_DC + / -	BI	C12 / D12		PORT6_IGBaseT_DC + / -	BI	17 / 19	
	PORT6_IGBaseT_DD + / -	BI	G12 / H12		PORT6_IGBaseT_DD + / -	BI	18 / 20	
	PORT7_IGBaseT_DA + / -	BI	A13 / B13		PORT7_IGBaseT_DA + / -	BI	21 / 23	
	PORT7_IGBaseT_DB + / -	BI	E13 / F13		PORT7_IGBaseT_DB + / -	BI	22 / 24	
	PORT7_IGBaseT_DC + / -	BI	C14 / D14		PORT7_IGBaseT_DC + / -	BI	25 / 27	
	PORT7_IGBaseT_DD + / -	BI	G14 / H14		PORT7_IGBaseT_DD + / -	BI	26 / 28	
	PORT8_IGBaseT_DA + / -	BI	A15 / B15		PORT8_IGBaseT_DA + / -	BI	33 / 35	
	PORT8_IGBaseT_DB + / -	BI	E15 / F15		PORT8_IGBaseT_DB + / -	BI	34 / 36	
	PORT8_IGBaseT_DC + / -	BI	C16 / D16		PORT8_IGBaseT_DC + / -	BI	37 / 39	
	PORT8_IGBaseT_DD + / -	BI	G16 / H16		PORT8_IGBaseT_DD + / -	BI	38 / 40	

NOTES: SEE SHEET 3

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SIZE	CAGE CODE	DOCUMENT NO.	REV.
C	77820	CF-020010-57X	A
SCALE: 1.0	REF: NONE	SHEET 4 OF 8	

CONNECTION LIST

FROM				CONVERSION	TO			
VPX CONNECTOR	DESCRIPTION	INPUT / OUTPUT	PIN		DESCRIPTION	INPUT / OUTPUT	PIN	CONNECTOR
P2	GND	OUTPUT	A2, A4, A6, A8, A10, A12, A14, A16, B2, B4, B6, B8, B10, B12, B14, B16, C1, C3, C5, C7, C9, C11, C13, C15, D1, D3, D5, D7, D9, D11, D13, D15, E2, E4, E6, E8, E10, E12, E14, E16, F2, F4, F6, F8, F10, F12, F14, F16, G1, G3, G5, G7, G9, G11, G13, G15, H1, H3, H5, H7, H9, H11, H13, H15 i2, i4, i6, i8, i10, i12, i14, i16	N/A	N/A			
	NC	N/A	A1, A3, A5, A7, A9, A11, A13, A15, B1, B3, B5, B7, B9, B11, B13, B15, C2, C4, C6, C8, C10, C12, C14, C16, D2, D4, D6, D8, D10, D12, D14, D16, E1, E3, E5, E7, E9, E11, E13, E15, F1, F3, F5, F7, F9, F11, F13, F15, G2, G4, G6, G8, G10, G12, G14, G16, H2, H4, H6, H8, H10, H12, H14, H16, i13, i15					
	MDC	INPUT	i1					
	MDIO	BI	i3					
	CV1_FAULT_(1-4)	BI	i5					
	CV2_FAULT_(1-2)	BI	i7					
	CV2_FAULT_(3-4)	BI	i9					
	I2C-SCL	INPUT	i11					
P3	GND	OUTPUT	A2, A4, A6, A8, A10, A12, A14, A16, B2, B4, B6, B8, B10, B12, B14, B16, C1, C3, C5, C7, C9, C11, C13, C15, D1, D3, D5, D7, D9, D11, D13, D15, E2, E4, E6, E8, E10, E12, E14, E16, F2, F4, F6, F8, F10, F12, F14, F16, G1, G3, G5, G7, G9, G11, G13, G15, H1, H3, H5, H7, H9, H11, H13, H15, i2, i4, i6, i8, i10, i12, i14, i16					
	NC	N/A	A1, A3, A5, A7, A9, A11, A13, A15, B1, B3, B5, B7, B9, B11, B13, B15, C2, C4, C6, C8, C10, C12, C14, C16, D2, D4, D6, D8, D10, D12, D14, D16, E1, E3, E5, E7, E9, E11, E13, E15, F1, F3, F5, F7, F9, F11, F13, F15, G2, G4, G6, G8, G10, G12, G14, G16, H2, H4, H6, H8, H10, H12, H14, H16, i1, i3, i5, i7, i9, i11, i13, i15					

NOTES: SEE SHEET 3

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CONNECTION LIST

(4)

FROM				CONVERSION	TO			
VPX CONNECTOR	DESCRIPTION	INPUT / OUTPUT	PIN		DESCRIPTION	INPUT / OUTPUT	PIN	CONNECTOR
P4	GND	OUTPUT	A2, A4, A6, A8, A10, A12, A14, A16, B2, B4, B6, B8, B10, B12, B14, B16, C1, C3, C5, C7, C9, C11, C13, C15, D1, D3, D5, D7, D9, D11, D13, D15, E2, E4, E6, E8, E10, E12, E14, E16, F2, F4, F6, F8, F10, F12, F14, F16, G1, G3, G5, G7, G9, G11, G13, G15, H1, H3, H5, H7, H9, H11, H13, H15, i2, i4, i6, i8, i10, i12, i14, i16	N/A				
	NC	N/A	i1, i3, i5, i7, i9, i13, i15					
	PORT1_XAUI_0_RX + / -	INPUT	A1 / B1	10G PHY	PORT1_10GBaseT_DA + / -	BI	1 / 3	J3
	PORT1_XAUI_0_TX + / -	OUTPUT	E1 / F1					
	PORT1_XAUI_1_RX + / -	INPUT	C2 / D2		PORT1_10GBaseT_DB + / -	BI	2 / 4	
	PORT1_XAUI_1_TX + / -	OUTPUT	G2 / H2					
	PORT1_XAUI_2_RX + / -	INPUT	A3 / B3		PORT1_10GBaseT_DC + / -	BI	5 / 7	
	PORT1_XAUI_2_TX + / -	OUTPUT	E3 / F3					
	PORT1_XAUI_3_RX + / -	INPUT	C4 / D4		PORT1_10GBaseT_DD + / -	BI	6 / 8	
	PORT1_XAUI_3_TX + / -	OUTPUT	G4 / H4					
	PORT2_XAUI_0_RX + / -	INPUT	A5 / B5	10G PHY	PORT2_10GBaseT_DA + / -	BI	13 / 15	
	PORT2_XAUI_0_TX + / -	OUTPUT	E5 / F5					
	PORT2_XAUI_1_RX + / -	INPUT	C6 / D6		PORT2_10GBaseT_DB + / -	BI	14 / 16	
	PORT2_XAUI_1_TX + / -	OUTPUT	G6 / H6					
	PORT2_XAUI_2_RX + / -	INPUT	A7 / B7		PORT2_10GBaseT_DC + / -	BI	17 / 19	
	PORT2_XAUI_2_TX + / -	OUTPUT	E7 / F7					
	PORT2_XAUI_3_RX + / -	INPUT	C8 / D8		PORT2_10GBaseT_DD + / -	BI	18 / 20	
	PORT2_XAUI_3_TX + / -	OUTPUT	G8 / H8					
	PORT3_XAUI_0_RX + / -	INPUT	A9 / B9	10G PHY	PORT3_10GBaseT_DA + / -	BI	21 / 23	
	PORT3_XAUI_0_TX + / -	OUTPUT	E9 / F9					
	PORT3_XAUI_1_RX + / -	INPUT	C10 / D10		PORT3_10GBaseT_DB + / -	BI	22 / 24	
	PORT3_XAUI_1_TX + / -	OUTPUT	G10 / H10					
	PORT3_XAUI_2_RX + / -	INPUT	A11 / B11		PORT3_10GBaseT_DC + / -	BI	25 / 27	
	PORT3_XAUI_2_TX + / -	OUTPUT	E11 / F11					
	PORT3_XAUI_3_RX + / -	INPUT	C12 / D12		PORT3_10GBaseT_DD + / -	BI	26 / 28	
	PORT3_XAUI_3_TX + / -	OUTPUT	G12 / H12					
	PORT4_XAUI_0_RX + / -	INPUT	A13 / B13	10G PHY	PORT4_10GBaseT_DA + / -	BI	33 / 35	
	PORT4_XAUI_0_TX + / -	OUTPUT	E13 / F13					
	PORT4_XAUI_1_RX + / -	INPUT	C14 / D14		PORT4_10GBaseT_DB + / -	BI	34 / 36	
	PORT4_XAUI_1_TX + / -	OUTPUT	G14 / H14					
	PORT4_XAUI_2_RX + / -	INPUT	A15 / B15		PORT4_10GBaseT_DC + / -	BI	37 / 39	
	PORT4_XAUI_2_TX + / -	OUTPUT	E15 / F15					
	PORT4_XAUI_3_RX + / -	INPUT	C16 / D16		PORT4_10GBaseT_DD + / -	BI	38 / 40	
	PORT4_XAUI_3_TX + / -	OUTPUT	G16 / H16					
	I2C-SDA	BI	i11	N/A				

NOTES: SEE SHEET 3

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CONNECTION LIST

FROM				CONVERSION	TO			
VPX CONNECTOR	DESCRIPTION	INPUT / OUTPUT	PIN		DESCRIPTION	INPUT / OUTPUT	PIN	CONNECTOR
P5	GND	OUTPUT	A2, A4, A6, A8, A10, A12, A14, A16, B2, B4, B6, B8, B10, B12, B14, B16, C1, C3, C5, C7, C9, C11, C13, C15, D1, D3, D5, D7, D9, D11, D13, D15, E2, E4, E6, E8, E10, E12, E14, E16, F2, F4, F6, F8, F10, F12, F14, F16, G1, G3, G5, G7, G9, G11, G13, G15, H1, H3, H5, H7, H9, H11, H13, H15, i2, i4, i6, i8, i10, i12, i14, i16	N/A				
	NC	N/A	i1, i3, i5, i7, i9, i11, i13, i15					
	PORT1_40GBaseKR4_RX0 + / -	INPUT	A1 / B1	E/O	40GBase-SR4 CH1_TX1	OUTPUT	A-1	J4
	PORT1_40GBaseKR4_RX1 + / -	INPUT	C2 / D2	E/O	40GBase-SR4 CH1_TX2	OUTPUT	A-2	
	PORT1_40GBaseKR4_RX2 + / -	INPUT	A3 / B3	E/O	40GBase-SR4 CH1_TX3	OUTPUT	A-3	
	PORT1_40GBaseKR4_RX3 + / -	INPUT	C4 / D4	E/O	40GBase-SR4 CH1_TX4	OUTPUT	A-4	
	PORT1_40GBaseKR4_TX3 + / -	OUTPUT	G4 / H4	E/O	40GBase-SR4 CH1_RX4	INPUT	A-9	
	PORT1_40GBaseKR4_TX2 + / -	OUTPUT	E3 / F3	E/O	40GBase-SR4 CH1_RX3	INPUT	A-10	
	PORT1_40GBaseKR4_TX1 + / -	OUTPUT	G2 / H2	E/O	40GBase-SR4 CH1_RX2	INPUT	A-11	
	PORT1_40GBaseKR4_TX0 + / -	OUTPUT	E1 / F1	E/O	40GBase-SR4 CH1_RX1	INPUT	A-12	
	PORT2_40GBaseKR4_RX0 + / -	INPUT	A5 / B5	E/O	40GBase-SR4 CH2_TX1	OUTPUT	A-13	
	PORT2_40GBaseKR4_RX1 + / -	INPUT	C6 / D6	E/O	40GBase-SR4 CH2_TX2	OUTPUT	A-14	
	PORT2_40GBaseKR4_RX2 + / -	INPUT	A7 / B7	E/O	40GBase-SR4 CH2_TX3	OUTPUT	A-15	
	PORT2_40GBaseKR4_RX3 + / -	INPUT	C8 / D8	E/O	40GBase-SR4 CH2_TX4	OUTPUT	A-16	
	PORT2_40GBaseKR4_TX3 + / -	OUTPUT	G8 / H8	E/O	40GBase-SR4 CH2_RX4	INPUT	A-21	
	PORT2_40GBaseKR4_TX2 + / -	OUTPUT	E7 / F7	E/O	40GBase-SR4 CH2_RX3	INPUT	A-22	
	PORT2_40GBaseKR4_TX1 + / -	OUTPUT	G6 / H6	E/O	40GBase-SR4 CH2_RX2	INPUT	A-23	
	PORT2_40GBaseKR4_TX0 + / -	OUTPUT	E5 / F5	E/O	40GBase-SR4 CH2_RX1	INPUT	A-24	
	PORT3_40GBaseKR4_RX0 + / -	INPUT	A9 / B9	E/O	40GBase-SR4 CH3_TX1	OUTPUT	A-25	
	PORT3_40GBaseKR4_RX1 + / -	INPUT	C10 / D10	E/O	40GBase-SR4 CH3_TX2	OUTPUT	A-26	
	PORT3_40GBaseKR4_RX2 + / -	INPUT	A11 / B11	E/O	40GBase-SR4 CH3_TX3	OUTPUT	A-27	
	PORT3_40GBaseKR4_RX3 + / -	INPUT	C12 / D12	E/O	40GBase-SR4 CH3_TX4	OUTPUT	A-28	
	PORT3_40GBaseKR4_TX3 + / -	OUTPUT	G12 / H12	E/O	40GBase-SR4 CH3_RX4	INPUT	A-33	
	PORT3_40GBaseKR4_TX2 + / -	OUTPUT	E11 / F11	E/O	40GBase-SR4 CH3_RX3	INPUT	A-34	
	PORT3_40GBaseKR4_TX1 + / -	OUTPUT	G10 / H10	E/O	40GBase-SR4 CH3_RX2	INPUT	A-35	
	PORT3_40GBaseKR4_TX0 + / -	OUTPUT	E9 / F9	E/O	40GBase-SR4 CH3_RX1	INPUT	A-36	
	PORT4_40GBaseKR4_RX0 + / -	INPUT	A13 / B13	E/O	40GBase-SR4 CH4_TX1	OUTPUT	A-37	
	PORT4_40GBaseKR4_RX1 + / -	INPUT	C14 / D14	E/O	40GBase-SR4 CH4_TX2	OUTPUT	A-38	
	PORT4_40GBaseKR4_RX2 + / -	INPUT	A15 / B15	E/O	40GBase-SR4 CH4_TX3	OUTPUT	A-39	
	PORT4_40GBaseKR4_RX3 + / -	INPUT	C16 / D16	E/O	40GBase-SR4 CH4_TX4	OUTPUT	A-40	
	PORT4_40GBaseKR4_TX3 + / -	OUTPUT	G16 / H16	E/O	40GBase-SR4 CH4_RX4	INPUT	A-45	
	PORT4_40GBaseKR4_TX2 + / -	OUTPUT	E15 / F15	E/O	40GBase-SR4 CH4_RX3	INPUT	A-46	
	PORT4_40GBaseKR4_TX1 + / -	OUTPUT	G14 / H14	E/O	40GBase-SR4 CH4_RX2	INPUT	A-47	
	PORT4_40GBaseKR4_TX0 + / -	OUTPUT	E13 / F13	E/O	40GBase-SR4 CH4_RX1	INPUT	A-48	

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CONNECTION LIST

FROM				CONVERSION	TO			
VPX CONNECTOR	DESCRIPTION	INPUT / OUTPUT	PIN		DESCRIPTION	INPUT / OUTPUT	PIN	CONNECTOR
P6	GND	OUTPUT	A2, A4, A6, A8, A10, A12, A14, A16, B2, B4, B6, B8, B10, B12, B14, B16, C1, C3, C5, C7, C9, C11, C13, C15, D1, D3, D5, D7, D9, D11, D13, D15, E2, E4, E6, E8, E10, E12, E14, E16, F2, F4, F6, F8, F10, F12, F14, F16, G1, G3, G5, G7, G9, G11, G13, G15, H1, H3, H5, H7, H9, H11, H13, H15, i2, i4, i6, i8, i10, i12, i14, i16	N/A				
	NC	N/A	i1, i3, i5, i7, i9, i13, i15					
	PORT5_40GBaseKR4_RX0 + / -	INPUT	A1 / B1	E/O	40GBase-SR4 CH5_TX1	OUTPUT	B-1	J4
	PORT5_40GBaseKR4_RX1 + / -	INPUT	C2 / D2	E/O	40GBase-SR4 CH5_TX2	OUTPUT	B-2	
	PORT5_40GBaseKR4_RX2 + / -	INPUT	A3 / B3	E/O	40GBase-SR4 CH5_TX3	OUTPUT	B-3	
	PORT5_40GBaseKR4_RX3 + / -	INPUT	C4 / D4	E/O	40GBase-SR4 CH5_TX4	OUTPUT	B-4	
	PORT5_40GBaseKR4_TX3 + / -	OUTPUT	G4 / H4	E/O	40GBase-SR4 CH5_RX4	INPUT	B-9	
	PORT5_40GBaseKR4_TX2 + / -	OUTPUT	E3 / F3	E/O	40GBase-SR4 CH5_RX3	INPUT	B-10	
	PORT5_40GBaseKR4_TX1 + / -	OUTPUT	G2 / H2	E/O	40GBase-SR4 CH5_RX2	INPUT	B-11	
	PORT5_40GBaseKR4_TX0 + / -	OUTPUT	E1 / F1	E/O	40GBase-SR4 CH5_RX1	INPUT	B-12	
	PORT6_40GBaseKR4_RX0 + / -	INPUT	A5 / B5	E/O	40GBase-SR4 CH6_TX1	OUTPUT	B-13	
	PORT6_40GBaseKR4_RX1 + / -	INPUT	C6 / D6	E/O	40GBase-SR4 CH6_TX2	OUTPUT	B-14	
	PORT6_40GBaseKR4_RX2 + / -	INPUT	A7 / B7	E/O	40GBase-SR4 CH6_TX3	OUTPUT	B-15	
	PORT6_40GBaseKR4_RX3 + / -	INPUT	C8 / D8	E/O	40GBase-SR4 CH6_TX4	OUTPUT	B-16	
	PORT6_40GBaseKR4_TX3 + / -	OUTPUT	G8 / H8	E/O	40GBase-SR4 CH6_RX4	INPUT	B-21	
	PORT6_40GBaseKR4_TX2 + / -	OUTPUT	E7 / F7	E/O	40GBase-SR4 CH6_RX3	INPUT	B-22	
	PORT6_40GBaseKR4_TX1 + / -	OUTPUT	G6 / H6	E/O	40GBase-SR4 CH6_RX2	INPUT	B-23	
	PORT6_40GBaseKR4_TX0 + / -	OUTPUT	E5 / F5	E/O	40GBase-SR4 CH6_RX1	INPUT	B-24	
	PORT7_40GBaseKR4_RX0 + / -	INPUT	A9 / B9	E/O	40GBase-SR4 CH7_TX1	OUTPUT	B-25	
	PORT7_40GBaseKR4_RX1 + / -	INPUT	C10 / D10	E/O	40GBase-SR4 CH7_TX2	OUTPUT	B-26	
	PORT7_40GBaseKR4_RX2 + / -	INPUT	A11 / B11	E/O	40GBase-SR4 CH7_TX3	OUTPUT	B-27	
	PORT7_40GBaseKR4_RX3 + / -	INPUT	C12 / D12	E/O	40GBase-SR4 CH7_TX4	OUTPUT	B-28	
	PORT7_40GBaseKR4_TX3 + / -	OUTPUT	G12 / H12	E/O	40GBase-SR4 CH7_RX4	INPUT	B-33	
	PORT7_40GBaseKR4_TX2 + / -	OUTPUT	E11 / F11	E/O	40GBase-SR4 CH7_RX3	INPUT	B-34	
	PORT7_40GBaseKR4_TX1 + / -	OUTPUT	G10 / H10	E/O	40GBase-SR4 CH7_RX2	INPUT	B-35	
	PORT7_40GBaseKR4_TX0 + / -	OUTPUT	E9 / F9	E/O	40GBase-SR4 CH7_RX1	INPUT	B-36	
	PORT8_40GBaseKR4_RX0 + / -	INPUT	A13 / B13	E/O	40GBase-SR4 CH8_TX1	OUTPUT	B-37	
	PORT8_40GBaseKR4_RX1 + / -	INPUT	C14 / D14	E/O	40GBase-SR4 CH8_TX2	OUTPUT	B-38	
	PORT8_40GBaseKR4_RX2 + / -	INPUT	A15 / B15	E/O	40GBase-SR4 CH8_TX3	OUTPUT	B-39	
	PORT8_40GBaseKR4_RX3 + / -	INPUT	C16 / D16	E/O	40GBase-SR4 CH8_TX4	OUTPUT	B-40	
	PORT8_40GBaseKR4_TX3 + / -	OUTPUT	G16 / H16	E/O	40GBase-SR4 CH8_RX4	INPUT	B-45	
	PORT8_40GBaseKR4_TX2 + / -	OUTPUT	E15 / F15	E/O	40GBase-SR4 CH8_RX3	INPUT	B-46	
	PORT8_40GBaseKR4_TX1 + / -	OUTPUT	G14 / H14	E/O	40GBase-SR4 CH8_RX2	INPUT	B-47	
	PORT8_40GBaseKR4_TX0 + / -	OUTPUT	E13 / F13	E/O	40GBase-SR4 CH8_RX1	INPUT	B-48	

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