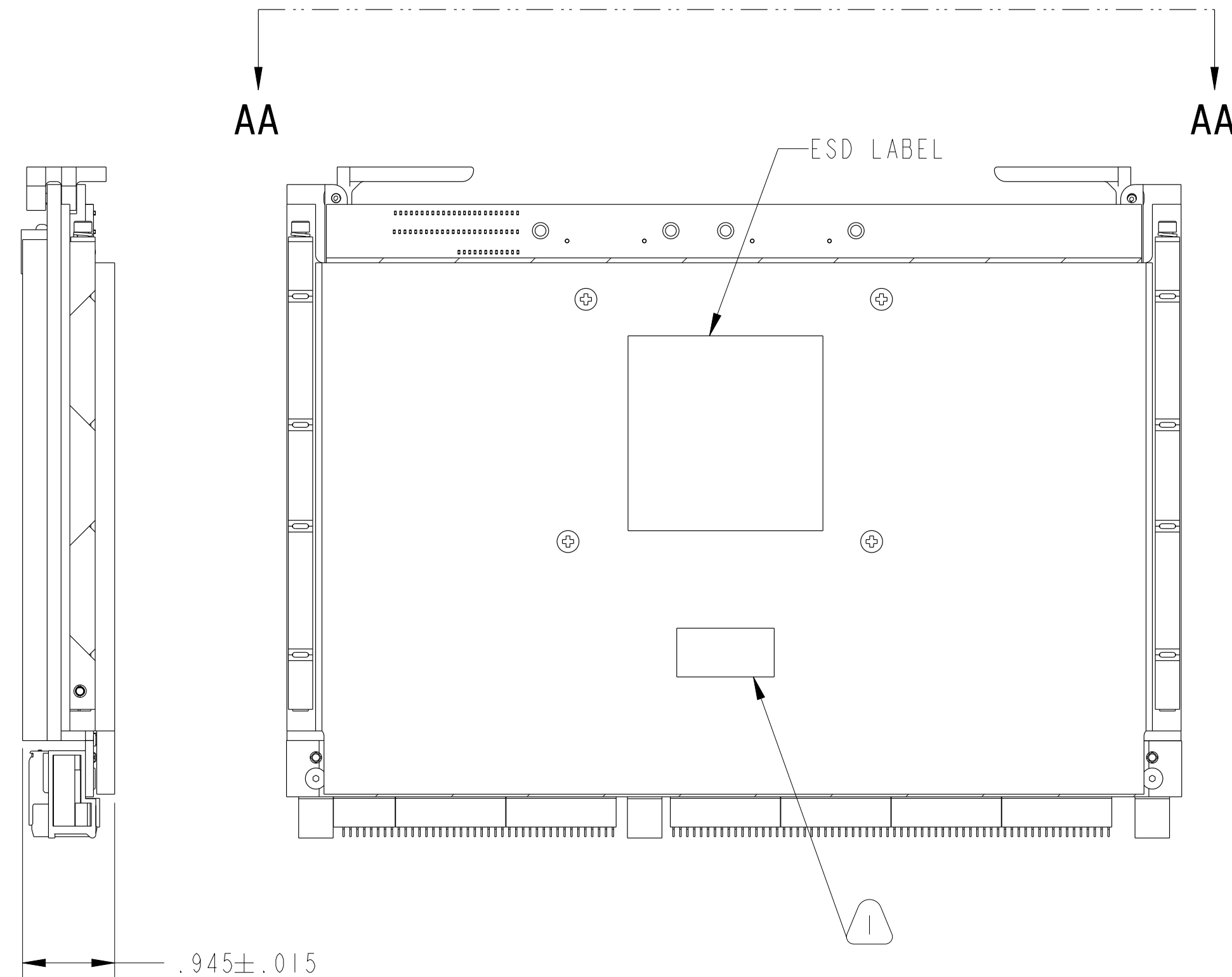
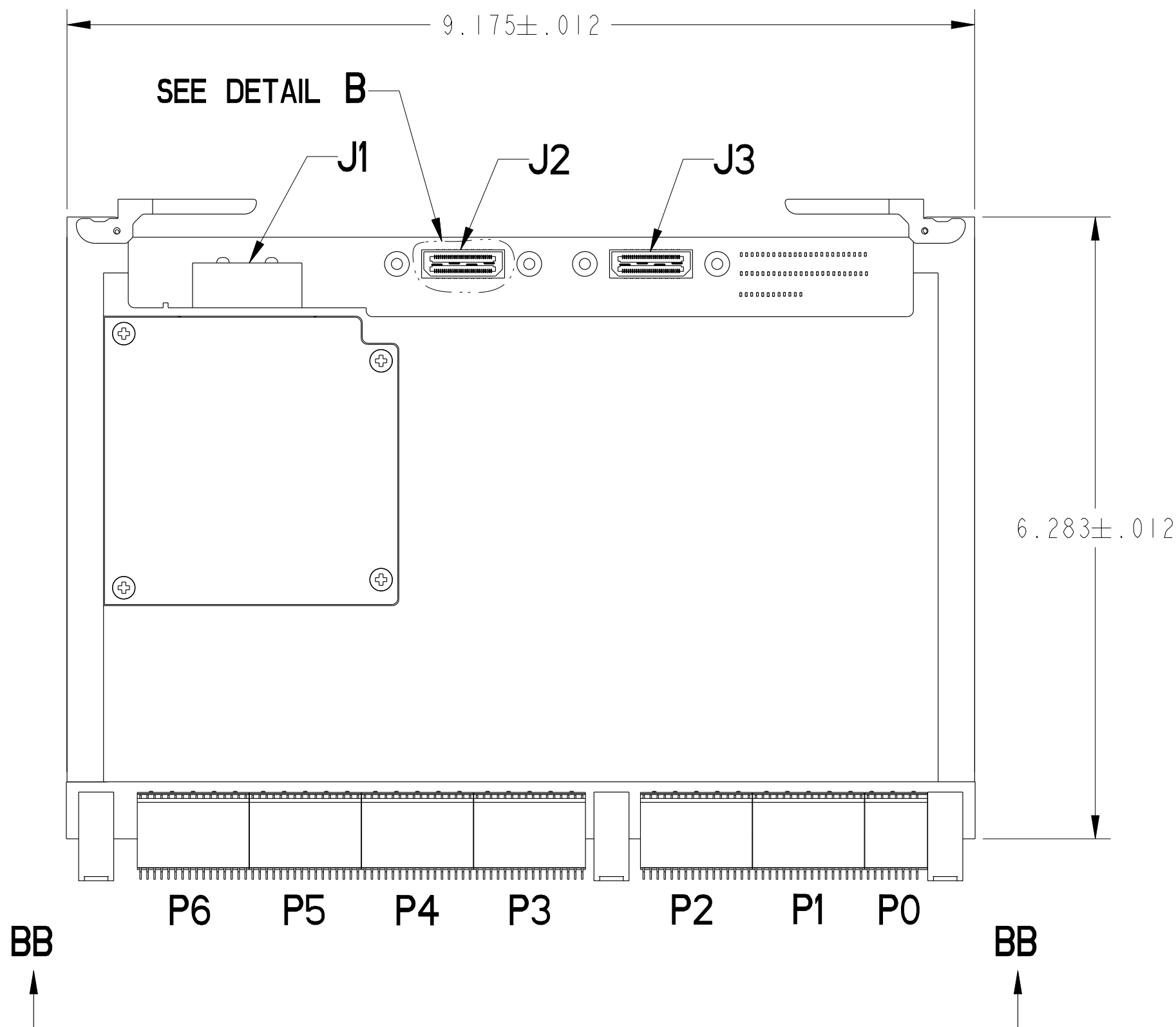


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REVISIONS M		
LTR	DESCRIPTION	DATE
A	INITIAL RELEASE	08/03/20

CF-020400-01X

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REV. A



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NOTES:

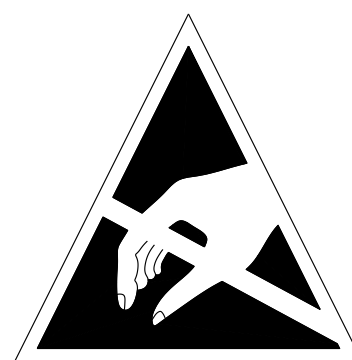
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REV. A



**ATTENTION**  
OBSERVE PRECAUTIONS  
FOR HANDLING  
ELECTROSTATIC  
SENSITIVE  
DEVICES

NONE

NEXT ASSEMBLY

PRO/ENGINEER INFORMATION

Pro/e Model Used:  
CF-020400-01X.ASSEM  
Drawing Name:  
CF-020400-01X

UNLESS OTHERWISE SPECIFIED	SPECIFICATIONS	POS	QTY	PART NUMBER	DESCRIPTION	NOTE
LINEAR DIMENSIONS ARE IN INCHES TOLERANCES: .XXXX = ±.0005 ANGLES= ±2° .XXX = ±.010 .XX = ±.03 .X = ±.1 DIM. & TOL. PER ASME Y14.5M; DRM PER MIL-STD-31000; OTHER Amphenol Stds. PER 9-3800 LEGENDS: =FLAG NOTE CALL OUT REFERENCE ONLY	MATERIAL SPEC.	PARTS LIST <b>AMPHENOL CORPORATION</b> 40-60 DELAWARE AVENUE SIDNEY, N.Y. 13858 <b>ETHERNET SWITCH</b> <b>VITA 48.2, 6U CARD</b>				
	NONE					
PROCESS SPEC.	SEE NOTE 1	APPROVALS PREPARED BY W. LEE ENGINEER IN CHARGE W. LEE DESIGN MANAGER J. ROTHROCK DESIGN ACTIVITY GROUP CI DATE 28-Jul-20 THIRD ANGLE PROJECTION				
				SIZE C SCALE: 0.8	CAGE CODE 77820 REF. CF-020011-48X	DOCUMENT NO. CF-020400-01X SHEET 1 OF 5

FORMAT: C-U-E-I

REV. A

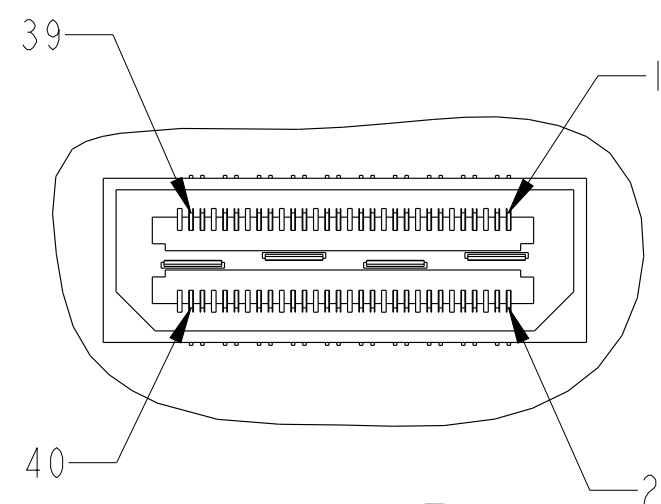
SHEET 1 OF 5

DOCUMENT NO. CF-020400-01X

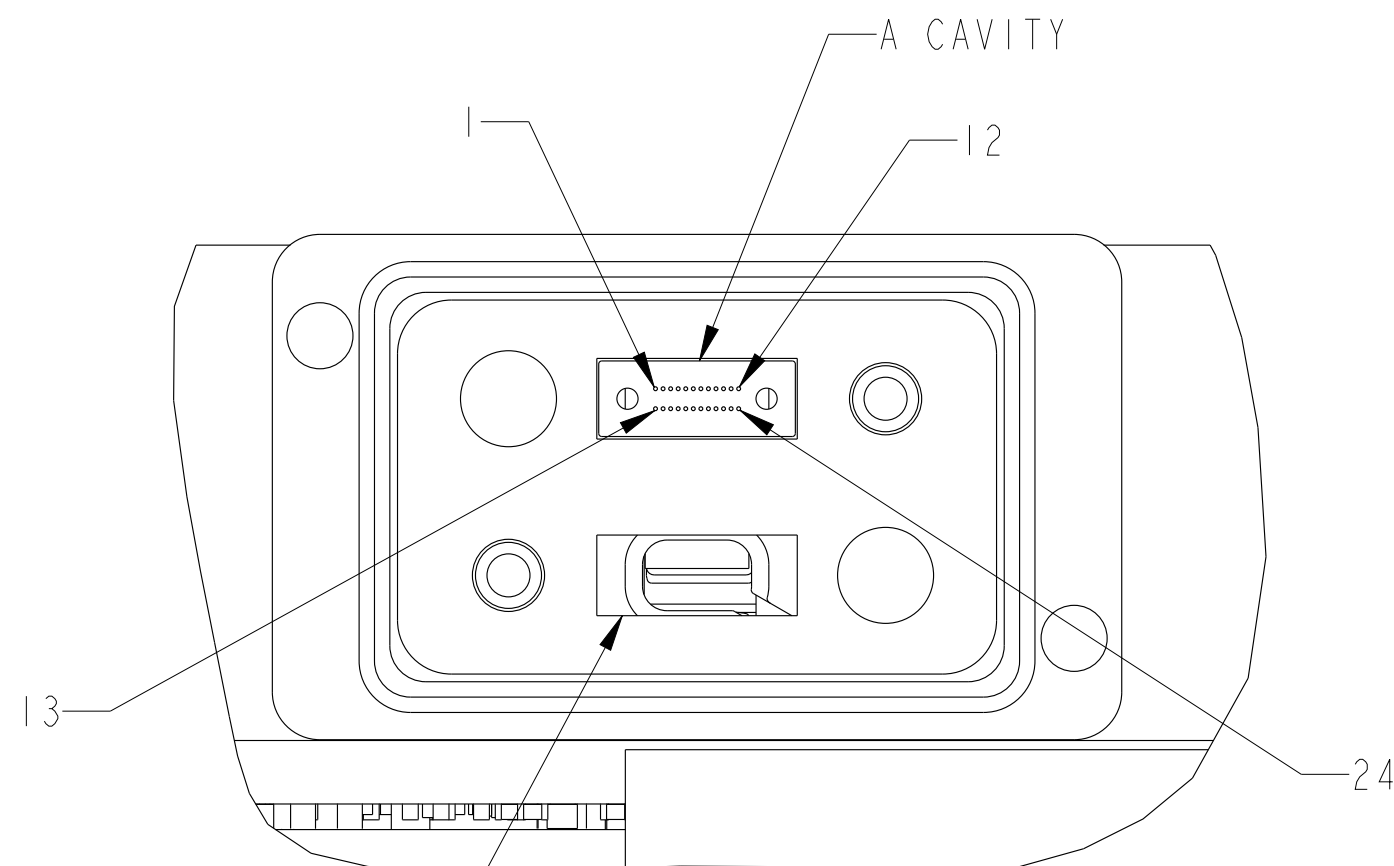
VERSION 2

REVISION A

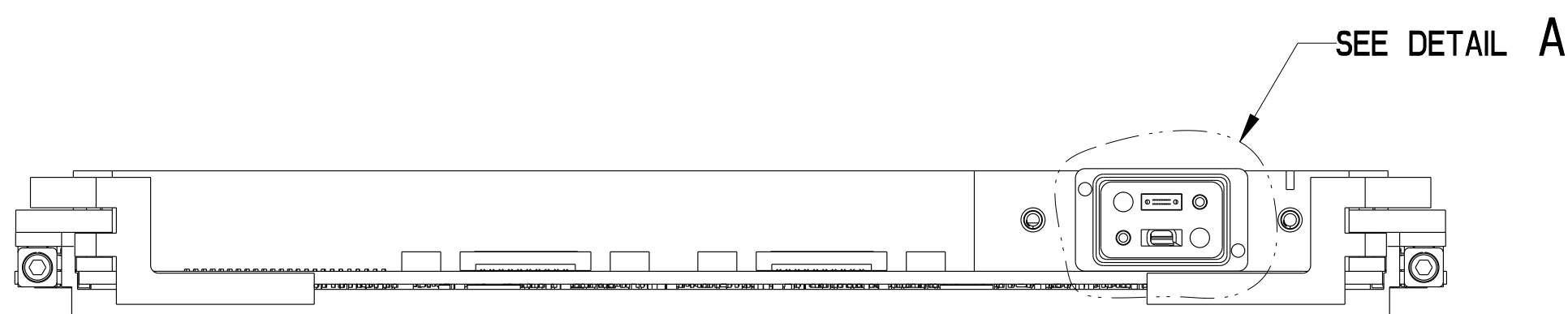
Eng PDM Information For Reference Only



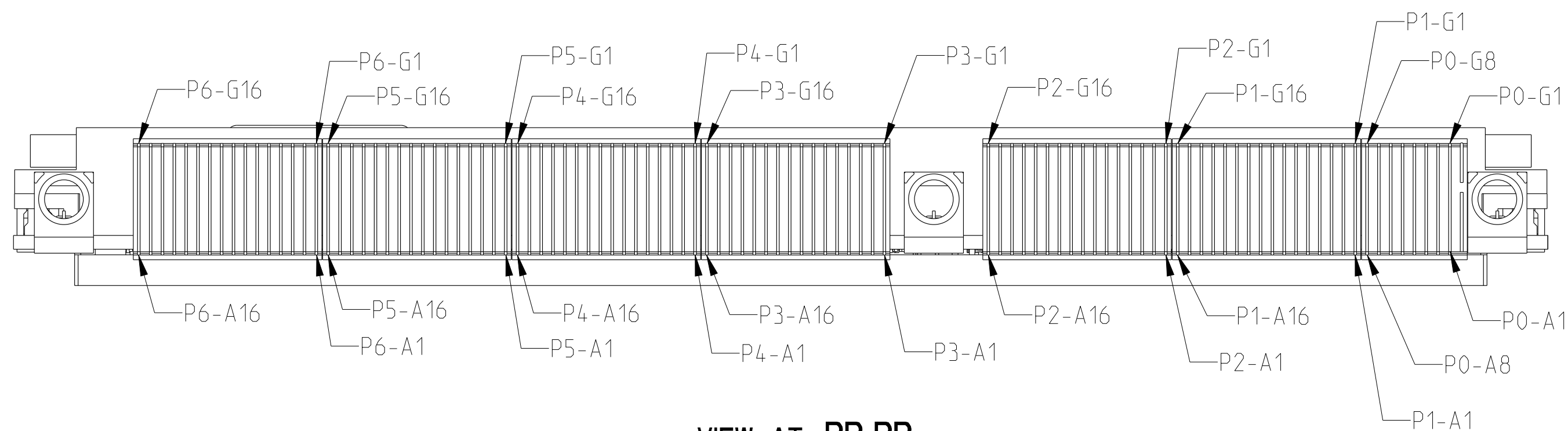
**DETAIL B**  
J2 AND J3 CONNECTORS  
SCALE 3.000



**DETAIL A**  
J1 CONNECTOR  
SCALE 4.000



**VIEW AT AA-AA**  
SCALE 1.000



**VIEW AT BB-BB**  
P0 THRU P6 CONNECTORS  
SCALE 1.500

SEE SHEET 3

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SIZE <b>C</b>	CAGE CODE <b>77820</b>	DOCUMENT NO. <b>CF-020400-01X</b>	REV. <b>A</b>
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D

C

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- 5. ASSEMBLY MEETS ALL APPLICABLE REQUIREMENTS OF VITA 48.2 SPECIFICATION.
- 4. SEE SHEETS 4 AND 5 FOR CONNECTOR I/O CHARTS.
- 3. PACKAGE PER PRODUCTION PROCESS SHEET.
- 2. ASSEMBLY CONTAINS ELECTROSTATIC DISCHARGE (ESD) SENSITIVE COMPONENTS. ASSEMBLY SHALL BE HANDLED, PACKAGED, AND SHIPPED TO MEET REQUIREMENTS OF ANSI/ESD-S-20.20 AND IPC-A-610.

1. MARK AMPHENOL, PART NUMBER AND SEVEN DIGIT SERIAL NUMBER ON LABEL AND ATTACH ON INDICATED SURFACE. SERIAL NUMBER PER 9-9172-3.

EXAMPLE: ASSEMBLY NUMBER 5 BUILT ON AUGUST 20 (WEEK 34), 2020.

AMPHENOL  
CF-020400-01X  
2034005

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FORMAT: C-0-L-E-1

PO I/O CHART

Table with 8 columns (PIN, A, B, C, D, E, F, G) and 8 rows of pin configurations for PO I/O CHART.

PI I/O CHART ( 1GBase-KX4 )

Table with 8 columns (PIN, A, B, C, D, E, F, G) and 16 rows of pin configurations for PI I/O CHART ( 1GBase-KX4 ).

P2 I/O CHART ( 10GBase-KX4 )

Table with 8 columns (PIN, A, B, C, D, E, F, G) and 16 rows of pin configurations for P2 I/O CHART ( 10GBase-KX4 ).

P3 I/O CHART ( 10GBase-KX4 )

Table with 8 columns (PIN, A, B, C, D, E, F, G) and 16 rows of pin configurations for P3 I/O CHART ( 10GBase-KX4 ).

SEE SHEET 3

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P4 I/O CHART ( 10GBase-KX4 )

Table with 8 columns (PIN, A, B, C, D, E, F, G) and 16 rows of pin configurations for P4 I/O CHART ( 10GBase-KX4 ).

P5 I/O CHART ( 10GBase-KX4 )

Table with 8 columns (PIN, A, B, C, D, E, F, G) and 16 rows of pin configurations for P5 I/O CHART ( 10GBase-KX4 ).

P6 I/O CHART ( 10GBase-KX4 )

Table with 8 columns (PIN, A, B, C, D, E, F, G) and 16 rows of pin configurations for P6 I/O CHART ( 10GBase-KX4 ).

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SHEET 4 OF 5

REV. A

C

B

A

FORMAT: C-U-E-I

Table with 4 columns: SIZE (C), CAGE CODE (77820), DOCUMENT NO. (CF-020400-01X), REV. (A). Includes SCALE: 0.8 and REF: CF-020011-48X.

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### J2 I/O CHART ( 10GBase-T )

PIN	SIGNAL	PIN	SIGNAL
1	P37_10G-T_DAP	2	P37_10G-T_DBP
3	P37_10G-T_DAN	4	P37_10G-T_DBN
5	P37_10G-T_DCP	6	P37_10G-T_DDP
7	P37_10G-T_DCN	8	P37_10G-T_DDN
9	--	10	--
11	--	12	--
13	P38_10G-T_DAP	14	P38_10G-T_DBP
15	P38_10G-T_DAN	16	P38_10G-T_DBN
17	P38_10G-T_DCP	18	P38_10G-T_DDP
19	P38_10G-T_DCN	20	P38_10G-T_DDN
21	P39_10G-T_DAP	22	P39_10G-T_DBP
23	P39_10G-T_DAN	24	P39_10G-T_DBN
25	P39_10G-T_DCP	26	P39_10G-T_DDP
27	P39_10G-T_DCN	28	P39_10G-T_DDN
29	--	30	--
31	--	32	--
33	P40_10G-T_DAP	34	P40_10G-T_DBP
35	P40_10G-T_DAN	36	P40_10G-T_DBN
37	P40_10G-T_DCP	38	P40_10G-T_DDP
39	P40_10G-T_DCN	40	P40_10G-T_DDN

### J1 I/O CHART (10GBase-SR)

PIN	SIGNAL	PIN	SIGNAL
1	P45_10G-SR_RX	13	P45_10G-SR_TX
2	P46_10G-SR_RX	14	P46_10G-SR_TX
3	P47_10G-SR_RX	15	P47_10G-SR_TX
4	P48_10G-SR_RX	16	P48_10G-SR_TX
5	P49_10G-SR_RX	17	P49_10G-SR_TX
6	P50_10G-SR_RX	18	P50_10G-SR_TX
7	P51_10G-SR_RX	19	P51_10G-SR_TX
8	P52_10G-SR_RX	20	P52_10G-SR_TX
9	P53_10G-SR_RX	21	P53_10G-SR_TX
10	P54_10G-SR_RX	22	P54_10G-SR_TX
11	P55_10G-SR_RX	23	P55_10G-SR_TX
12	P56_10G-SR_RX	24	P56_10G-SR_TX

### J3 I/O CHART ( 1GBase-T )

PIN	SIGNAL	PIN	SIGNAL
1	P41_1G-T_DAP	2	P41_1G-T_DBP
3	P41_1G-T_DAN	4	P41_1G-T_DBN
5	P41_1G-T_DCP	6	P41_1G-T_DDP
7	P41_1G-T_DCN	8	P41_1G-T_DDN
9	--	10	--
11	--	12	--
13	P42_1G-T_DAP	14	P42_1G-T_DBP
15	P42_1G-T_DAN	16	P42_1G-T_DBN
17	P42_1G-T_DCP	18	P42_1G-T_DDP
19	P42_1G-T_DCN	20	P42_1G-T_DDN
21	P43_1G-T_DAP	22	P43_1G-T_DBP
23	P43_1G-T_DAN	24	P43_1G-T_DBN
25	P43_1G-T_DCP	26	P43_1G-T_DDP
27	P43_1G-T_DCN	28	P43_1G-T_DDN
29	--	30	--
31	--	32	--
33	P44_1G-T_DAP	34	P44_1G-T_DBP
35	P44_1G-T_DAN	36	P44_1G-T_DBN
37	P44_1G-T_DCP	38	P44_1G-T_DDP
39	P44_1G-T_DCN	40	P44_1G-T_DDN

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