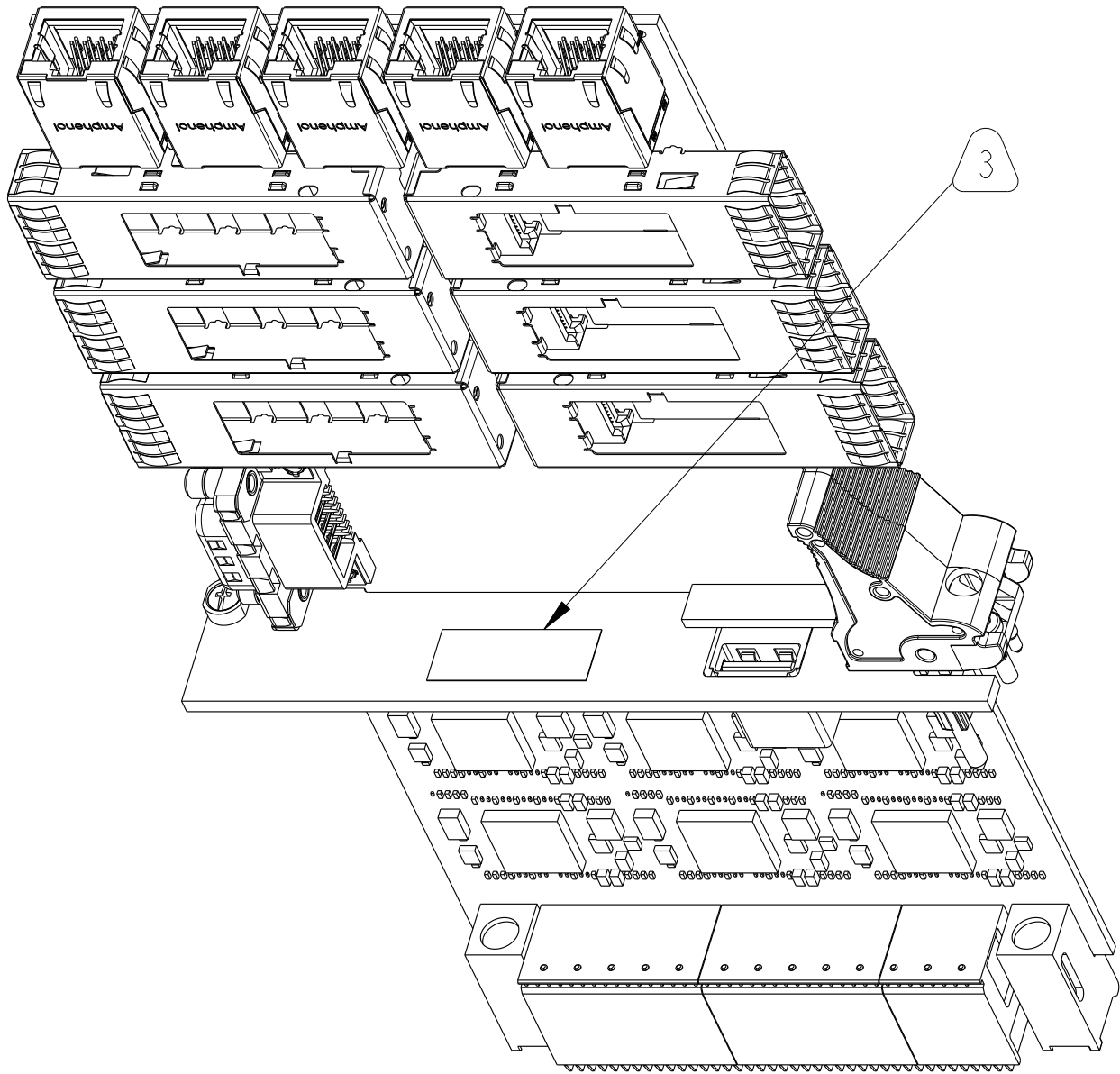


REV. A	SHEET 1 OF 4	DOCUMENT NO. CF-020400-022R
REVISIONS M		
LTR	DESCRIPTION	DATE
A	INITIAL RELEASE	01/30/20



ISOMETRIC VIEW
SCALE 1.000

6. QSFP MODULES (QSFP-SR4-40G) ARE NOT SHOWN. 6 PCS OF MODULES SHALL BE SHIPPED UNASSEMBLED IN A SEPARATE BAG.
5. ASSEMBLY MEETS THE APPLICABLE REQUIREMENTS OF ANSI/VITA 46.10 REAR TRANSITION MODULE FOR VPX.
4. PACKAGE PER PRODUCTION PROCESS SHEET.
3. MARK LABEL WITH "AMPHENOL", PART NUMBER, AND SEVEN DIGIT SERIAL NUMBER AND ATTACH ON INDICATED SURFACE. SERIAL NUMBER PER 9-9172-3
- EXAMPLE: FOR ASSEMBLY NUMBER 7 BUILT ON MAY 20 (WEEK 21), 2020
- AMPHENOL
CF-020400-022R
2021007
2. PARTS TO BE PACKAGED IN METALLIZED SHIELDING BAGS AND LABELED PER ANSI/ESD S541-2003.
1. ASSEMBLY CONTAINS ELECTROSTATIC DISCHARGE (ESD) SENSITIVE COMPONENTS. ASSEMBLY SHALL BE HANDLED, PACKAGED, AND SHIPPED TO MEET REQUIREMENTS OF ANSI/ESD-S-20.20 AND IPC-A-610.

NOTES:

THE USE OF THIS DOCUMENT IS UNLIMITED. HOWEVER, DOCUMENTS REFERENCED HEREON MAY CONTAIN LIMITED RIGHTS DATA.

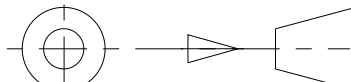
RELEASED / Feb. 03 2020, 2:33:21 PM

DOCUMENT NO. CF-020400-022R

SHEET 1 OF 4

REV. A

PRO/ENGINEER INFORMATION
Pro/e Model Used:
CF-020400-022R.ASSEM
Drawing Name:
CF-020400-022R

UNLESS OTHERWISE SPECIFIED		SPECIFICATIONS		POS	QTY	PART NUMBER	DESCRIPTION	NOTE	
DIM. & TOL. PER ASME Y14.5M; DRM PER MIL-STD-31000; OTHER Amphenol Slds. PER 9-3800 LEGENDS: =FLAG NOTE CALL OUT REFERENCE ONLY 		MATERIAL SPEC.	PARTS LIST						
		NONE	APPROVALS		DATE	AMPHENOL CORPORATION 40-60 DELAWARE AVENUE SIDNEY, N.Y. 13838			
		PREPARED BY ENGINEER IN CHARGE DESIGN MANAGER DESIGN ACTIVITY GROUP		30-Jan-20					
		PROCESS SPEC.	CI		3U RTM ASSEMBLY				
		9-9172-3	THIRD ANGLE PROJECTION						
		9-9172-3				SIZE C	CAGE CODE 77820	DOCUMENT NO. CF-020400-022R	REV. A
				SCALE: 1.0		REF. CF-020011-57X	SHEET 1 OF 4		



ATTENTION
OBSERVE PRECAUTIONS
FOR HANDLING
ELECTROSTATIC
SENSITIVE
DEVICES

DOCUMENT NO. CF-020400-022R

SHEET 1 OF 4

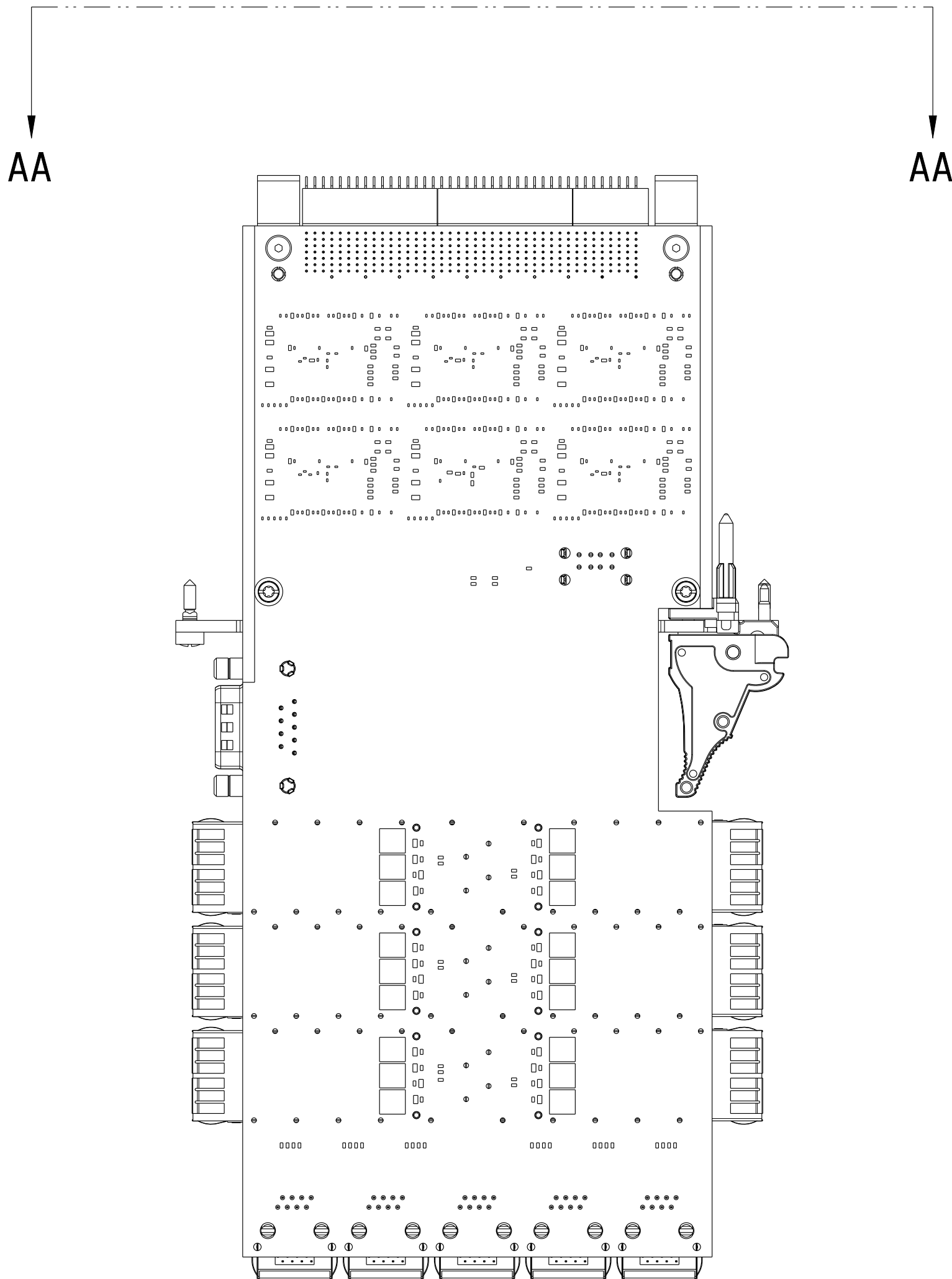
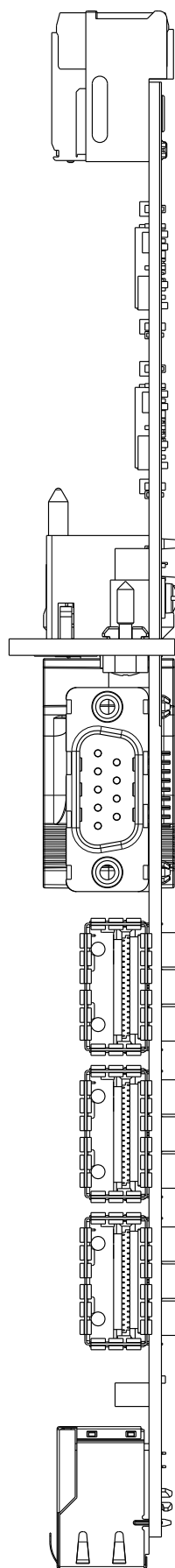
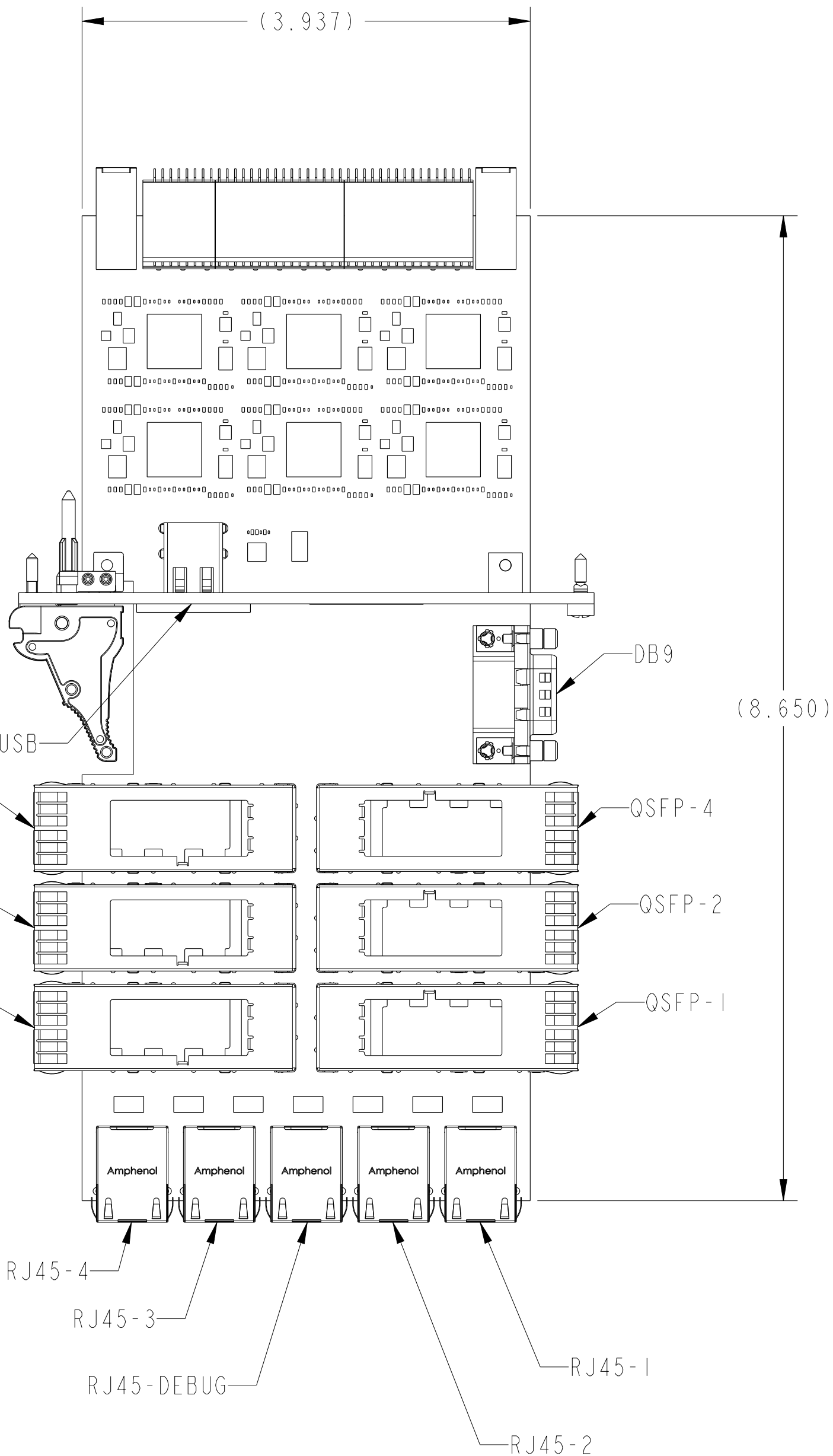
REV. A

C

B

A

FORMAT: C-0-E-1



SEE SHEET 1

NOTES:

THE USE OF THIS DOCUMENT IS UNLIMITED. HOWEVER, DOCUMENTS REFERENCED HEREON MAY CONTAIN LIMITED RIGHTS DATA.

RELEASED / Feb. 03 2020, 2:33:23 PM

CF-020400-022R

SHEET 2 OF 4

REV. A

SIZE	CAGE CODE	DOCUMENT NO.	REV.
C	77820	CF-020400-022R	A
SCALE: 1.0		REF: CF-020011-57X	SHEET 2 OF 4

4

3

2

REV. A

SHEET 3 OF 4

CF-020400-022R

DOCUMENT NO. CF-020400-022R

SHEET 3 OF 4

REV. A

J0 CONNECTOR I/O

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
G	12.0V	12.0V	5.0V	N/C	N/C	N/C	TCK	GND	USB_D+	GND	USB_D-	GND	XSMI_MDIO	GND	XSMI_MDC	GND
F	12.0V	12.0V	5.0V	N/C	N/C	N/C	GND	N/C	GND	PORT2 10G-KR_RX-	GND	PORT4 10G-KR_RX-	GND	PORT6 10G-KR_RX-	GND	PORT8 10G-KR_RX-
E	12.0V	12.0V	5.0V	GND	GND	GND	TDO	N/C	PORT1 10G-KR_RX-	PORT2 10G-KR_RX+	PORT3 10G-KR_RX-	PORT4 10G-KR_RX+	PORT5 10G-KR_RX-	PORT6 10G-KR_RX+	PORT7 10G-KR_RX-	PORT8 10G-KR_RX+
D	N/C	N/C	N/C	N/C	N/C	N/C	TDI	GND	PORT1 10G-KR_RX+	GND	PORT3 10G-KR_RX+	GND	PORT5 10G-KR_RX+	GND	PORT7 10G-KR_RX+	GND
C	3.3V	3.3V	5.0V	GND	GND	GND	GND	N/C	GND	PORT2 10G-KR_TX-	GND	PORT4 10G-KR_TX-	GND	PORT6 10G-KR_TX-	GND	PORT8 10G-KR_TX-
B	3.3V	3.3V	5.0V	SYSRST#	N/C	N/C	TMS	N/C	PORT1 10G-KR_TX-	PORT2 10G-KR_TX+	PORT3 10G-KR_TX-	PORT4 10G-KR_TX+	PORT5 10G-KR_TX-	PORT6 10G-KR_TX+	PORT7 10G-KR_TX-	PORT8 10G-KR_TX+
A	3.3V	3.3V	5.0V	N/C	N/C	N/C	N/C	GND	PORT1 10G-KR_TX+	GND	PORT3 10G-KR_TX+	GND	PORT5 10G-KR_TX+	GND	PORT7 10G-KR_TX+	GND

J1 CONNECTOR I/O

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
G	I2C_SDA	GND	I2C_SCL	GND	CPU RXD-232	GND	CPU TXD-232	GND	CPU_DEBUG 1G-T_DA+	GND	CPU_DEBUG 1G-T_DA-	GND	CPU_DEBUG 1G-T_DB+	GND	CPU_DEBUG 1G-T_DB-	GND
F	GND	PORT10 10G-KR_RX-	GND	PORT12 10G-KR_RX-	GND	PORT14 10G-KR_RX-	GND	PORT16 10G-KR_RX-	GND	PORT18 10G-KR_RX-	GND	PORT20 10G-KR_RX-	GND	PORT22 10G-KR_RX-	GND	PORT24 10G-KR_RX-
E	PORT9 10G-KR_RX-	PORT10 10G-KR_RX+	PORT11 10G-KR_RX-	PORT12 10G-KR_RX+	PORT13 10G-KR_RX-	PORT14 10G-KR_RX+	PORT15 10G-KR_RX-	PORT16 10G-KR_RX+	PORT17 10G-KR_RX-	PORT18 10G-KR_RX+	PORT19 10G-KR_RX-	PORT20 10G-KR_RX+	PORT21 10G-KR_RX-	PORT22 10G-KR_RX+	PORT23 10G-KR_RX-	PORT24 10G-KR_RX+
D	PORT9 10G-KR_RX+	GND	PORT11 10G-KR_RX+	GND	PORT13 10G-KR_RX+	GND	PORT15 10G-KR_RX+	GND	PORT17 10G-KR_RX+	GND	PORT19 10G-KR_RX+	GND	PORT21 10G-KR_RX+	GND	PORT23 10G-KR_RX+	GND
C	GND	PORT10 10G-KR_TX-	GND	PORT12 10G-KR_TX-	GND	PORT14 10G-KR_TX-	GND	PORT16 10G-KR_TX-	GND	PORT18 10G-KR_TX-	GND	PORT20 10G-KR_TX-	GND	PORT22 10G-KR_TX-	GND	PORT24 10G-KR_TX-
B	PORT9 10G-KR_TX-	PORT10 10G-KR_TX+	PORT11 10G-KR_TX-	PORT12 10G-KR_TX+	PORT13 10G-KR_TX-	PORT14 10G-KR_TX+	PORT15 10G-KR_TX-	PORT16 10G-KR_TX+	PORT17 10G-KR_TX-	PORT18 10G-KR_TX+	PORT19 10G-KR_TX-	PORT20 10G-KR_TX+	PORT21 10G-KR_TX-	PORT22 10G-KR_TX+	PORT23 10G-KR_TX-	PORT24 10G-KR_TX+
A	PORT9 10G-KR_TX+	GND	PORT11 10G-KR_TX+	GND	PORT13 10G-KR_TX+	GND	PORT15 10G-KR_TX+	GND	PORT17 10G-KR_TX+	GND	PORT19 10G-KR_TX+	GND	PORT21 10G-KR_TX+	GND	PORT23 10G-KR_TX+	GND

J2 CONNECTOR I/O

	1	2	3	4	5	6	7	8
G	CPU_DEBUG 1G-T_DC+	GND	CPU_DEBUG 1G-T_DC-	GND	CPU_DEBUG 1G-T_DD+	GND	CPU_DEBUG 1G-T_DD-	GND
F	GND	PORT1 10G-T_DD+	GND	PORT2 10G-T_DD+	GND	PORT3 10G-T_DD+	GND	PORT4 10G-T_DD+
E	PORT1 10G-T_DB+	PORT1 10G-T_DD-	PORT2 10G-T_DB+	PORT2 10G-T_DD-	PORT3 10G-T_DB+	PORT3 10G-T_DD-	PORT4 10G-T_DB+	PORT4 10G-T_DD-
D	PORT1 10G-T_DB-	GND	PORT2 10G-T_DB-	GND	PORT3 10G-T_DB-	GND	PORT4 10G-T_DB-	GND
C	GND	PORT1 10G-T_DC+	GND	PORT2 10G-T_DC+	GND	PORT3 10G-T_DC+	GND	PORT4 10G-T_DC+
B	PORT1 10G-T_DA+	PORT1 10G-T_DC-	PORT2 10G-T_DA+	PORT2 10G-T_DC-	PORT3 10G-T_DA+	PORT3 10G-T_DC-	PORT4 10G-T_DA+	PORT4 10G-T_DC-
A	PORT1 10G-T_DA-	GND	PORT2 10G-T_DA-	GND	PORT3 10G-T_DA-	GND	PORT4 10G-T_DA-	GND

VIEW AT AA-AA

SCALE 1.500

SEE SHEET 1

NOTES:

THE USE OF THIS DOCUMENT IS UNLIMITED. HOWEVER, DOCUMENTS REFERENCED HEREON MAY CONTAIN LIMITED RIGHTS DATA.

RELEASED / Feb. 03 2020, 2:33:23 PM

DOCUMENT NO. CF-020400-022R

SHEET 3 OF 4

REV. A

4

3

2

1

REV. A

SHEET 3 OF 4

CF-020400-022R

DOCUMENT NO. CF-020400-022R

SHEET 3 OF 4

REV. A

Eng. PDM Information For Reference Only

REVISION A

VERSION 3

