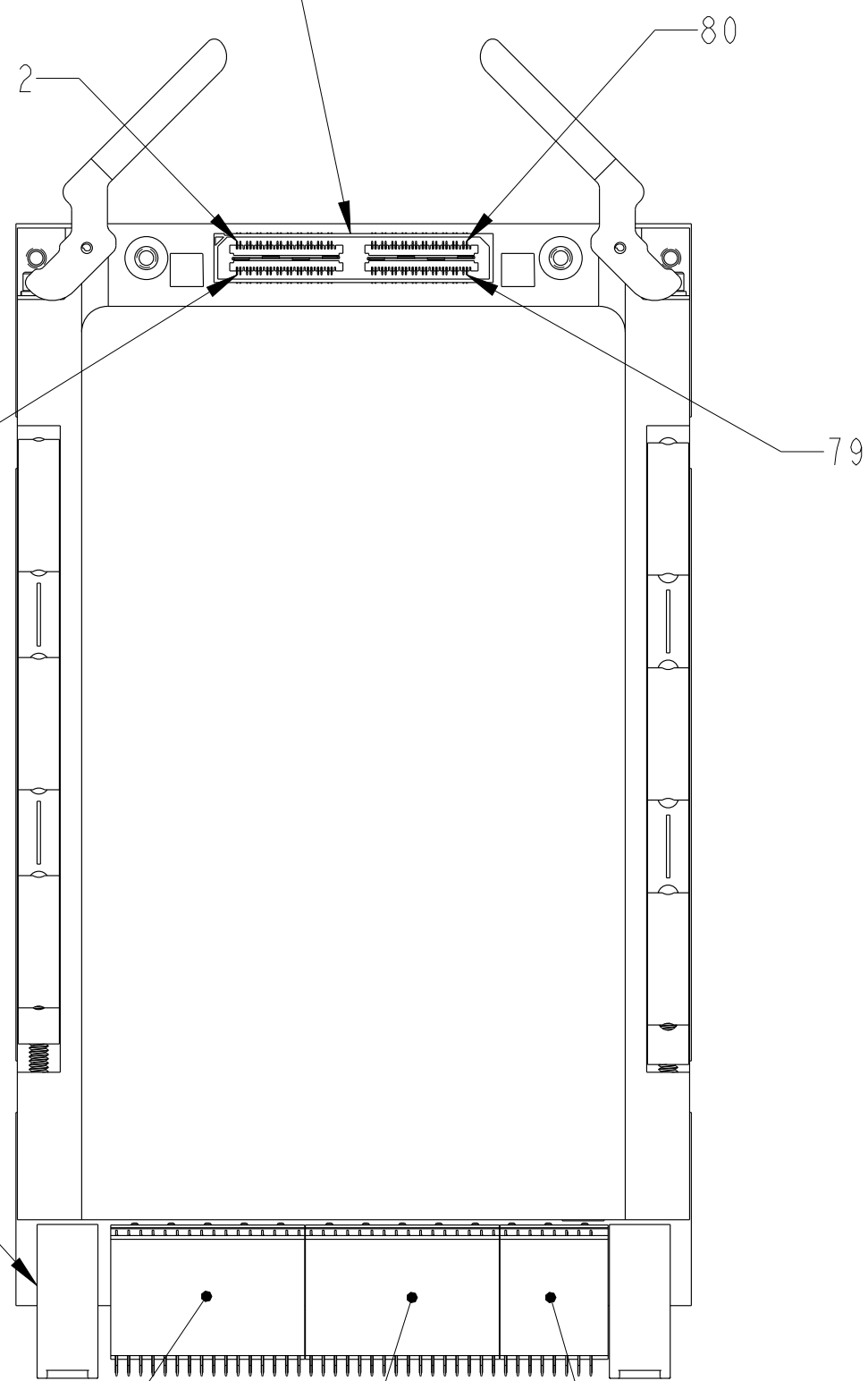


REV. A	SHEET 1 OF 3	DOCUMENT NO. CF-020400-022
REVISIONS M		
LTR	DESCRIPTION	DATE
A	INITIAL RELEASE	08/18/20

J2 CONNECTOR

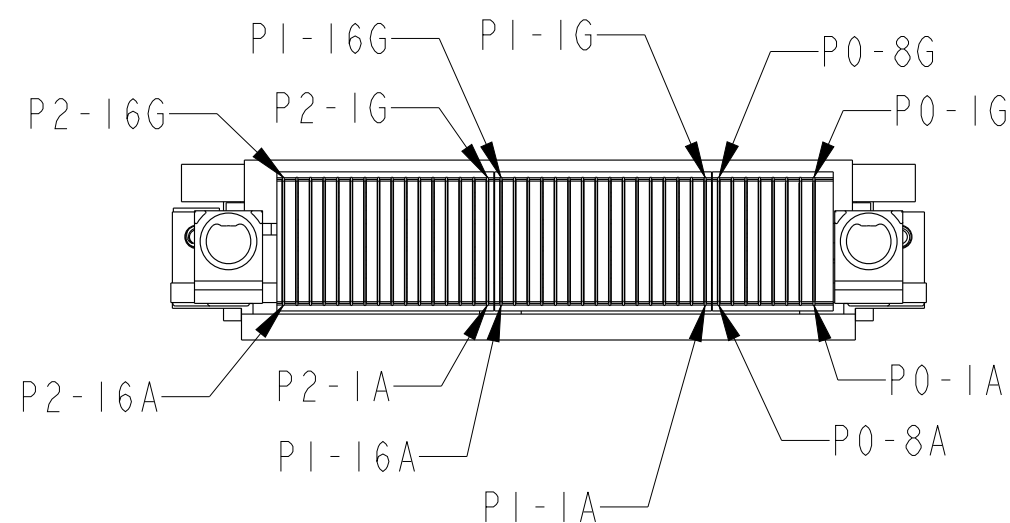


GUIDE BLOCK
12 O'CLOCK KEY'D
2000713-1 OR EQUIV.
2 PLACES

P2 CONNECTOR

P1 CONNECTOR

P0 CONNECTOR



SEE SHEET 3

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SHEET 1 OF 3

REV. A

PRO/ENGINEER INFORMATION

Pro/e Model Used:
CF-020400-022.ASSEM
Drawing Name:
CF-020400-022

NONE

NEXT ASSEMBLY

UNLESS OTHERWISE SPECIFIED

LINEAR DIMENSIONS ARE IN INCHES
TOLERANCES:
.XXXX = ±.0005 ANGLES= ±2°
.XXX = ±.010
.XX = ±.03
.X = ±.1

DIM. & TOL. PER ASME Y14.5M;
DRM PER MIL-STD-31000;
OTHER Amphenol Stds. PER 9-3800

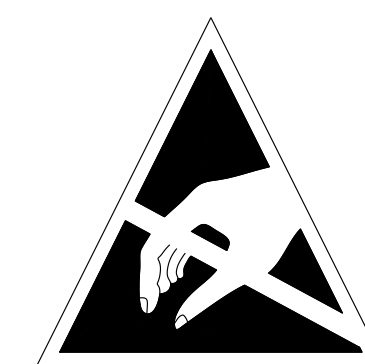
LEGENDS:
= FLAG NOTE CALL OUT
REFERENCE ONLY

SPECIFICATIONS

MATERIAL SPEC.
NONE

PROCESS SPEC.
9-9172-3

POS	QTY	PART NUMBER	DESCRIPTION	NOTE
PARTS LIST				
<p>APPROVALS</p> <p>PREPARED BY: W. LEE</p> <p>ENGINEER IN CHARGE: W. LEE</p> <p>DESIGN MANAGER: J. ROTHROCK</p> <p>DATE: 14-Aug-20</p> <p>DESIGN ACTIVITY GROUP: CI</p>				
<p>THIRD ANGLE PROJECTION</p>				
<p>AMPHENOL CORPORATION</p> <p>40-60 DELAWARE AVENUE SIDNEY, N.Y. 13858</p> <p>3U VPX SWITCH CONDUCTION COOLING 36 PORTS</p>				
SIZE C	CAGE CODE 77820	DOCUMENT NO. CF-020400-022	REV. A	
SCALE: 1.0		REF. CF-020400-026	SHEET 1 OF 3	



ATTENTION
OBSERVE PRECAUTIONS
FOR HANDLING
ELECTROSTATIC
SENSITIVE
DEVICES

Eng. PDM Information
For Reference Only

REV. A
SHEET 1 OF 3

DOCUMENT NO.
CF-020400-022

P0 I/O CHART

	G	F	E	D	C	B	A
1	+12V	+12V	+12V	NC	+3.3V	+3.3V	+3.3V
2	+12V	+12V	+12V	NC	+3.3V	+3.3V	+3.3V
3	+5V	+5V	+5V	NC	+5V	+5V	+5V
4	NC	NC	GND	NC	GND	SYRST#	NC
5	NC	NC	GND	NC	GND	NC	NC
6	NC	NC	GND	NC	GND	NC	NC
7	SWITCH JTAG_TCLK(NC)	GND	SWITCH JTAG_TDO(NC)	SWITCH JTAG_TDI(NC)	GND	SWITCH JTAG_TMS(NC)	NC
8	GND	NC	NC	GND	NC	NC	GND

J2 I/O CHART

ID	SIGNAL	ID	SIGNAL	ID	SIGNAL	ID	SIGNAL
1	P5_10G-T_DA+	2	P5_10G-T_DB+	41	P9_10G-T_DA+	42	P9_10G-T_DB+
3	P5_10G-T_DA-	4	P5_10G-T_DB-	43	P9_10G-T_DA-	44	P9_10G-T_DB-
5	P5_10G-T_DC+	6	P5_10G-T_DD+	45	P9_10G-T_DC+	46	P9_10G-T_DD+
7	P5_10G-T_DC-	8	P5_10G-T_DD-	47	P9_10G-T_DC-	48	P9_10G-T_DD-
9	+5V	10	GND	49	+5V	50	GND
11	+5V	12	GND	51	+5V	52	GND
13	P6_10G-T_DA+	14	P6_10G-T_DB+	53	P10_10G-T_DA+	54	P10_10G-T_DB+
15	P6_10G-T_DA-	16	P6_10G-T_DB-	55	P10_10G-T_DA-	56	P10_10G-T_DB-
17	P6_10G-T_DC+	18	P6_10G-T_DD+	57	P10_10G-T_DC+	58	P10_10G-T_DD+
19	P6_10G-T_DC-	20	P6_10G-T_DD-	59	P10_10G-T_DC-	60	P10_10G-T_DD-
21	P7_10G-T_DA+	22	P7_10G-T_DB+	61	P11_10G-T_DA+	62	P11_10G-T_DB+
23	P7_10G-T_DA-	24	P7_10G-T_DB-	63	P11_10G-T_DA-	64	P11_10G-T_DB-
25	P7_10G-T_DC+	26	P7_10G-T_DD+	65	P11_10G-T_DC+	66	P11_10G-T_DD+
27	P7_10G-T_DC-	28	P7_10G-T_DD-	67	P11_10G-T_DC-	68	P11_10G-T_DD-
29	+3.3V	30	GND	69	+3.3V	70	GND
31	+3.3V	32	GND	71	+3.3V	72	GND
33	P8_10G-T_DA+	34	P8_10G-T_DB+	73	P12_10G-T_DA+	74	P12_10G-T_DB+
35	P8_10G-T_DA-	36	P8_10G-T_DB-	75	P12_10G-T_DA-	76	P12_10G-T_DB-
37	P8_10G-T_DC+	38	P8_10G-T_DD+	77	P12_10G-T_DC+	78	P12_10G-T_DD+
39	P8_10G-T_DC-	40	P8_10G-T_DD-	79	P12_10G-T_DC-	80	P12_10G-T_DD-
G1	GND	G2	GND	G5	GND	G6	GND
G3	GND	G4	GND	G7	GND	G8	GND

P1 I/O CHART

	G	F	E	D	C	B	A
1	SWITCH_USB_D+	GND	P1_10G-KR_TX#	P1_10G-KR_TX	GND	P1_10G-KR_RX#	P1_10G-KR_RX
2	GND	P2_10G-KR_TX#	P2_10G-KR_TX	GND	P2_10G-KR_RX#	P2_10G-KR_RX	GND
3	SWITCH_USB_D-	GND	P3_10G-KR_TX#	P3_10G-KR_TX	GND	P3_10G-KR_RX#	P3_10G-KR_RX
4	GND	P4_10G-KR_TX#	P4_10G-KR_TX	GND	P4_10G-KR_RX#	P4_10G-KR_RX	GND
5	SWITCH_XSMI_MDIO	GND	P5_10G-KR_TX#	P5_10G-KR_TX	GND	P5_10G-KR_RX#	P5_10G-KR_RX
6	GND	P6_10G-KR_TX#	P6_10G-KR_TX	GND	P6_10G-KR_RX#	P6_10G-KR_RX	GND
7	SWITCH_XSMI_MDC	GND	P7_10G-KR_TX#	P7_10G-KR_TX	GND	P7_10G-KR_RX#	P7_10G-KR_RX
8	GND	P8_10G-KR_TX#	P8_10G-KR_TX	GND	P8_10G-KR_RX#	P8_10G-KR_RX	GND
9	SWITCH_I2C_SDA	GND	P9_10G-KR_TX#	P9_10G-KR_TX	GND	P9_10G-KR_RX#	P9_10G-KR_RX
10	GND	P10_10G-KR_TX#	P10_10G-KR_TX	GND	P10_10G-KR_RX#	P10_10G-KR_RX	GND
11	SWITCH_I2C_SCL	GND	P11_10G-KR_TX#	P11_10G-KR_TX	GND	P11_10G-KR_RX#	P11_10G-KR_RX
12	GND	P12_10G-KR_TX#	P12_10G-KR_TX	GND	P12_10G-KR_RX#	P12_10G-KR_RX	GND
13	CPU_RS232_TXD	GND	P13_10G-KR_TX#	P13_10G-KR_TX	GND	P13_10G-KR_RX#	P13_10G-KR_RX
14	GND	P14_10G-KR_TX#	P14_10G-KR_TX	GND	P14_10G-KR_RX#	P14_10G-KR_RX	GND
15	CPU_RS232_RXD	GND	P15_10G-KR_TX#	P15_10G-KR_TX	GND	P15_10G-KR_RX#	P15_10G-KR_RX
16	GND	P16_10G-KR_TX#	P16_10G-KR_TX	GND	P16_10G-KR_RX#	P16_10G-KR_RX	GND

P2 I/O CHART

	G	F	E	D	C	B	A
1	CPU_DEBUG_1G-T_DA	GND	P17_10G-KR_TX#	P17_10G-KR_TX	GND	P17_10G-KR_RX#	P17_10G-KR_RX
2	GND	P18_10G-KR_TX#	P18_10G-KR_TX	GND	P18_10G-KR_RX#	P18_10G-KR_RX	GND
3	CPU_DEBUG_1G-T_DA#	GND	P19_10G-KR_TX#	P19_10G-KR_TX	GND	P19_10G-KR_RX#	P19_10G-KR_RX
4	GND	P20_10G-KR_TX#	P20_10G-KR_TX	GND	P20_10G-KR_RX#	P20_10G-KR_RX	GND
5	CPU_DEBUG_1G-T_DB	GND	P21_10G-KR_TX#	P21_10G-KR_TX	GND	P21_10G-KR_RX#	P21_10G-KR_RX
6	GND	P22_10G-KR_TX#	P22_10G-KR_TX	GND	P22_10G-KR_RX#	P22_10G-KR_RX	GND
7	CPU_DEBUG_1G-T_DB#	GND	P23_10G-KR_TX#	P23_10G-KR_TX	GND	P23_10G-KR_RX#	P23_10G-KR_RX
8	GND	P24_10G-KR_TX#	P24_10G-KR_TX	GND	P24_10G-KR_RX#	P24_10G-KR_RX	GND
9	CPU_DEBUG_1G-T_DC	GND	P1_10G-T_DB-	P1_10G-T_DB+	GND	P1_10G-T_DA-	P1_10G-T_DA+
10	GND	P1_10G-T_DD-	P1_10G-T_DD+	GND	P1_10G-T_DC-	P1_10G-T_DC+	GND
11	CPU_DEBUG_1G-T_DC#	GND	P2_10G-T_DB-	P2_10G-T_DB+	GND	P2_10G-T_DA-	P2_10G-T_DA+
12	GND	P2_10G-T_DD-	P2_10G-T_DD+	GND	P2_10G-T_DC-	P2_10G-T_DC+	GND
13	CPU_DEBUG_1G-T_DD	GND	P3_10G-T_DB-	P3_10G-T_DB+	GND	P3_10G-T_DA-	P3_10G-T_DA+
14	GND	P3_10G-T_DD-	P3_10G-T_DD+	GND	P3_10G-T_DC-	P3_10G-T_DC+	GND
15	CPU_DEBUG_1G-T_DD#	GND	P4_10G-T_DB-	P4_10G-T_DB+	GND	P4_10G-T_DA-	P4_10G-T_DA+
16	GND	P4_10G-T_DD-	P4_10G-T_DD+	GND	P4_10G-T_DC-	P4_10G-T_DC+	GND

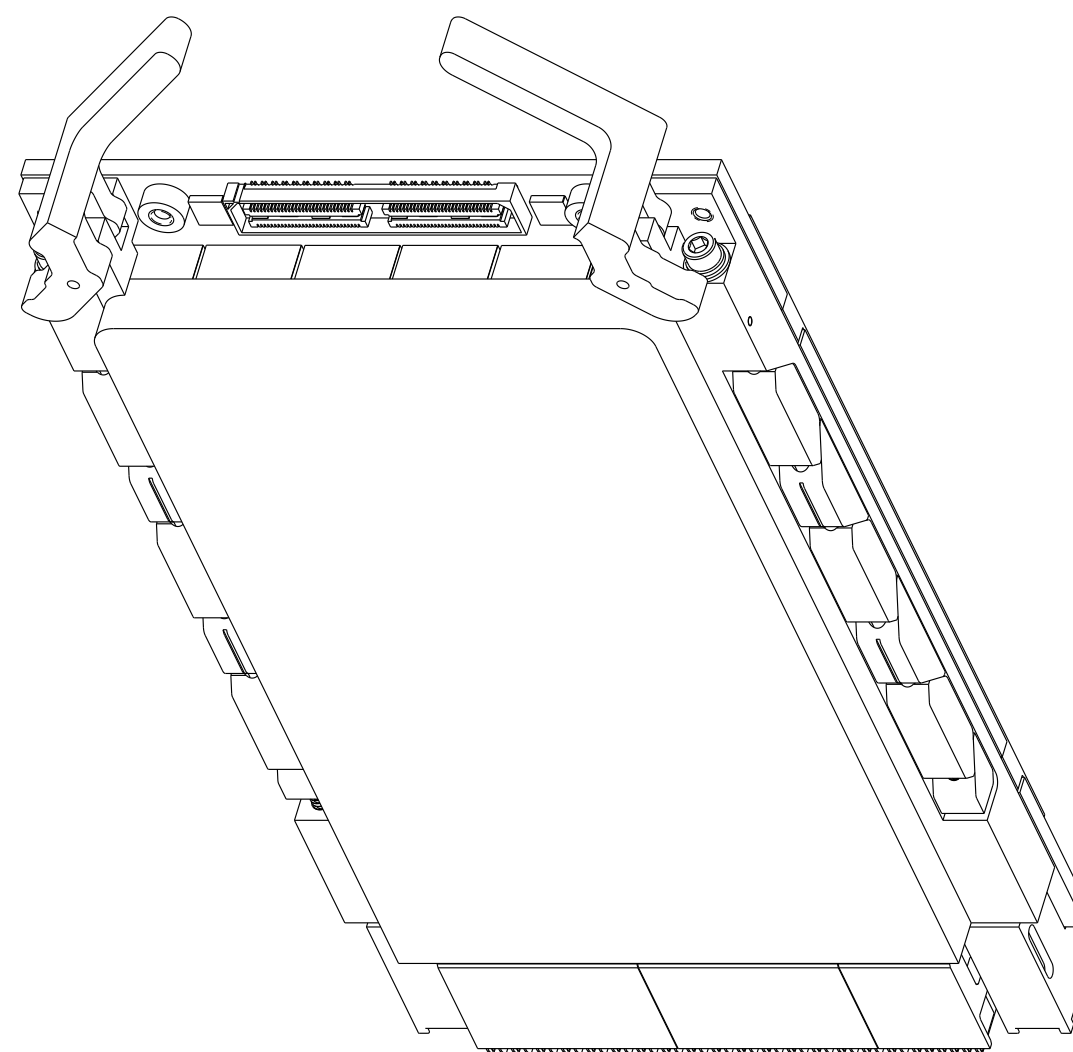
SEE SHEET 3

NOTES:

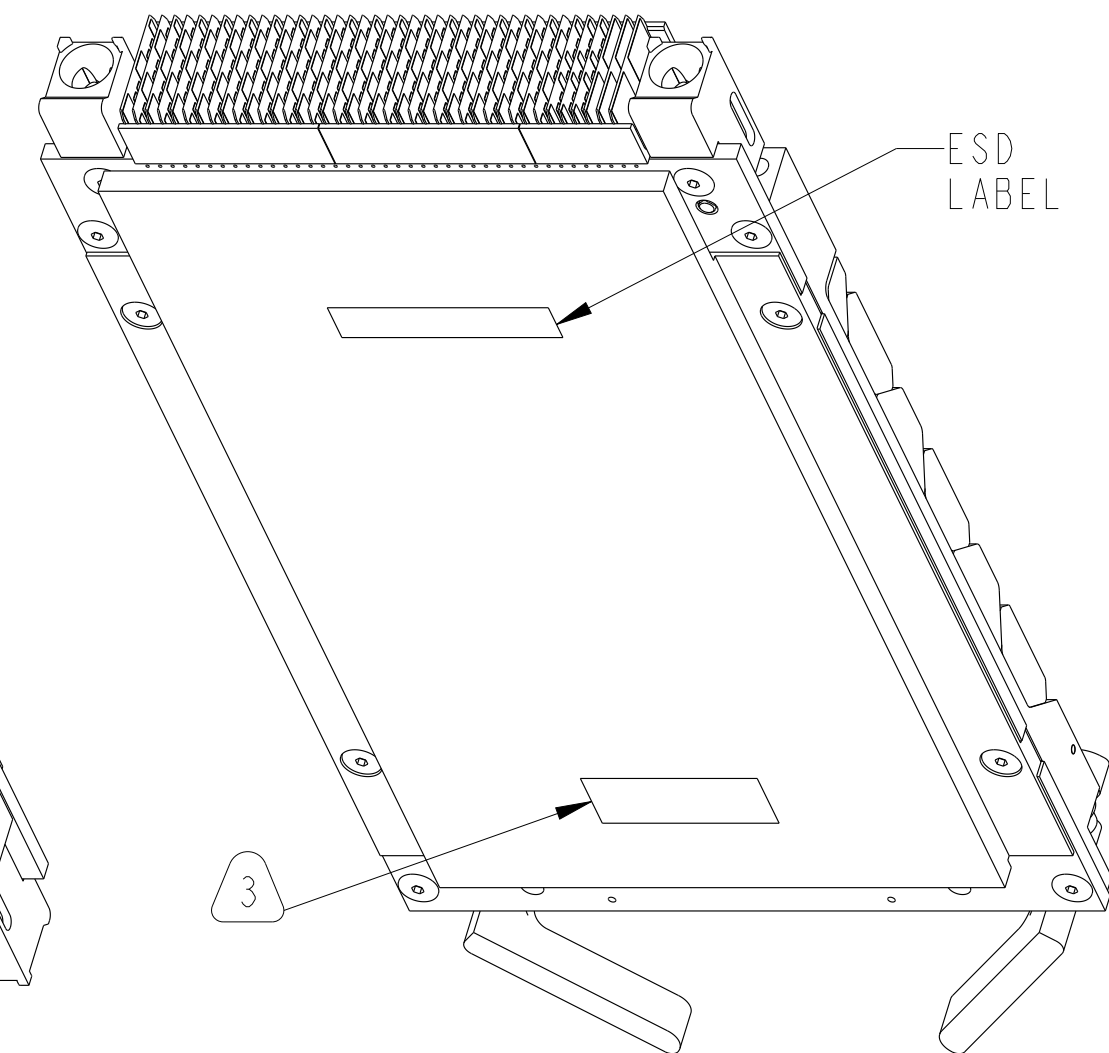
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SIZE C	CAGE CODE 77820	DOCUMENT NO. CF-020400-022	REV. A
SCALE: 1.0		REF: CF-020400-026	SHEET 2 OF 3



TOP ISOMETRIC VIEW
SCALE 1.000



BOTTOM ISOMETRIC VIEW
SCALE 1.000

6. J2 CONNECTOR MATING CABLE:
SAMTEC HQDP-040-()-TTR-()-()-()
(PARENTHESIS TO BE FILLED PER SAMTEC HQDP DATASHEET FOR DESIRED TYPE)

5. OPERATING TEMPERATURE: -40°C TO +85°C

4. MARKING ON LABEL MAY DIFFER FROM DRAWING BASED ON CUSTOMER SPECIFIC ORDER REQUIREMENTS.

3. MARK LABEL WITH "AMPHENOL", PART NUMBER, AND SEVEN DIGIT SERIAL NUMBER AND ATTACH ON INDICATED SURFACE. SERIAL NUMBER PER 9-9172-3

EXAMPLE: FOR ASSEMBLY NUMBER 7 BUILT ON OCT 20 (WEEK 43), 2020
AMPHENOL
CF-020400-022
2043007

2. PACKAGE PER PRODUCTION PROCESS SHEET.

1. ASSEMBLY CONTAINS ELECTROSTATIC DISCHARGE (ESD) SENSITIVE COMPONENTS. ASSEMBLY SHALL BE HANDLED, PACKAGED, AND SHIPPED TO MEET REQUIREMENTS OF ANSI/ESD-S-20.20 AND IPC-A-610.

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